The final part of the book describes two complete applications of hardware/software codesign. These examples are given with adequate application background, so that the system-level specification and the design refinement process is clear. The examples are fully implemented as prototypes in Field Programmable Gate Arrays (FPGA), and the reader has access to the full source code of these designs. The first application is a co-processor of the Trivium stream-cipher algorithm. This coprocessor is used for streaming-encryption applications. The second application is a co-processor for the evaluation of digital rotation functions (CORDIC).