PART III: Low-power and high-resolution ADC’s

For ADC’s low-power and high-resolution are important specifications. In fact in most cases the ratio of the two is important. At the design of the program several aspects have led to the specific choice of speakers. First of all, top-class specialists, from all over the world, that are currently working in this field should be found. Second, the program should both show a tutorial character and have specialistic depth. Third: it should address both the developments in business and in the academic field, so the developments in the universities. Fourth, the relevant architectures should addressed. Finally, aspects ranging from application, architecture, algorithm, circuit design, and influence of technology trend should be addressed in a balanced way. I think we really succeeded in that.

Most papers address sigma-delta converters, which are clearly the most popular ones nowadays in this field; one paper (Patrick Quinn, Xilinx) specifically addresses a new concept for algorithmic and pipelined converters. The applications range from communication (Kathleen Philips, Philips), via broadcast (Andrea Baschirotto, University of Lecce), to sensor application (Terri Fiez, Oregon State University). The paper of Ovidiu Bajdechi (Delft University, now with Broadcom) focusses on generic and systematic design aspects for sigma-delta converters, and another, the paper of Libin Yao (Katholieke Universiteit Leuven) discusses sigma-delta in the context of the evolution of CMOS processes.

Arthur van Roermund