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M. Alexe U. Gösele (Eds.)

Wafer Bonding

Applications and Technology

With 372 Figures and 25 Tables



Springer

Dr. M. Alexe
Prof. Dr. U. Gösele
Max Planck Institute of Microstructure Physics
Weinberg 2, 06120 Halle (Saale), Germany
E-mail: malexe@mpi-halle.de, goesele@mpi-halle.de

Series Editors:

Professor Robert Hull
University of Virginia
Dept. of Materials Science and Engineering
Thornton Hall
Charlottesville, VA 22903-2442, USA

Professor Jürgen Parisi
Universität Oldenburg, Fachbereich Physik
Abt. Energie- und Halbleiterforschung
Carl-von-Ossietzky-Strasse 9–11
26129 Oldenburg, Germany

Professor R. M. Osgood, Jr.
Microelectronics Science Laboratory
Department of Electrical Engineering
Columbia University
Seeley W. Mudd Building
New York, NY 10027, USA

Professor Hans Warlimont
Institut für Festkörper-
und Werkstofforschung,
Helmholtzstrasse 20
01069 Dresden, Germany

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Preface

Wafer bonding, also known as direct wafer bonding or wafer fusion, has developed from an almost obscure niche technology in the 1980s to a versatile base technology which is more and more applied in actual high-tech products. The present book will not concentrate so much on the basics and the science of wafer bonding which have been described in many hundreds of original publications, conference proceedings, numerous review articles and books, but rather on more applied and technological aspects of wafer bonding. Consequently, we have asked many authors from companies and sometimes also from competing companies to give their point of view, including perspectives on actual production and economic issues. Fortunately only in a few cases were we turned down by companies which were not willing to describe their substantial activities in the general area of wafer bonding. If the reader misses some of the obvious players in the field then this absence is certainly not due to the lack of trying on the part of the editors.

Purposely the book starts with a wide-ranging chapter on the history of patents in wafer bonding which gives a very general overview on the development of wafer bonding from the medieval ages over the time in which first developments could actually be patented to the more recent patent history.

The main commercial application of wafer bonding is in the area of silicon-on-insulator (SOI) technology. The basics of SOI technology therefore is described in a special chapter by one of the pioneers in the field of SOI devices. Two of the main technologies to fabricate thin film SOI wafers by wafer bonding (UNIBOND and ELTRAN) are described by representatives of the respective companies, SOITEC and Canon. The use of wafer bonding for future advanced digital electronic nano-devices is described by an author from IBM's Watson Research Labs in a chapter also touching on strained silicon devices and SiGe technology. A subsequent chapter is devoted to the very different fabrication of much thicker SOI layers in the range of many microns and their applications in numerous power and high voltage and analog devices presented in a contribution from Analog Devices.

Most of the subsequent chapters written by authors from public research organisations and universities deal with processes and devices which are not yet in production but clearly have the potential to get there. We have asked the authors to mention also technologies which could be considered as competing with wafer bonding and many authors followed our suggestion which allows the reader to get some information also on competing technologies. There are special chapters which deal mainly with partly competing and partly complementary technologies such as laser lift-off and crystal-ion slicing. The hybrid integration of optoelec-

tronics and microelectronics as well as the integration of ferroelectric layers and silicon by wafer bonding are described in two chapters. The use of wafer bonding for the fabrication of three-dimensional photonic crystals, described in the chapter from Kyoto University, is surprising and technologically elegant. One chapter coming from a Fraunhofer Institute is devoted to the question of how bonded wafers can be most efficiently debonded again, as this is of major interest for many practical applications. The final two chapters deal with wafer bonding approaches in optoelectronics, partly already in production, and with the development of new alternative substrates with single crystalline SiC layers, which will become important as much less expensive substitutes for bulk SiC wafers.

Let us finally mention that we are convinced that the enhanced design freedom in materials integration which is enabled by wafer bonding approaches will lead to many more unexpected technological solutions in the future. It appears to us that in the area of materials integration and the resulting devices, in many cases we are now more restricted by our limited imagination than by limitations in technology.

Halle, February 2004

*Marin Alexe
Ulrich M. Gösele*

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List of Contributors

Marin Alexe

Max Planck Institute
of Microstructure Physics,
06120 Halle,
Germany

Bernard Aspar

CEA-LETI, Département
des Technologies Silicium,
17 rue des Martyrs,
38054 Grenoble Cedex 9,
France

André J. Auberton-Hervé

Soitec S A,
Parc Technologique des Fontaines,
38926 Crolles Cedex,
France

Jörg Bagdahn

Fraunhofer-Institut
für Werkstoffmechanik
06120 Halle,
Germany

George K. Celler

Soitec USA,
2 Centennial Drive,
Peabody, MA 01960,
USA

Nathan W. Cheung

Department of Electrical Engineer-
ing and Computer Sciences,
University of California,
Berkeley, CA
USA

Jean-Pierre Colinge

Department of Electrical
and Computer Engineering,
University of California,
Davis, CA 95616
USA

Kathryn W. Guarini

IBM Research,
T.J. Watson Research Center,
Yorktown Heights,
New York 10598,
USA

Léa Di Cioccio

CEA-DRT-LETI,
17 rue des Martyrs,
38054 Grenoble,
France

Jan Haisma

72 Kornoeljelaan
5552 RD Valkeswaard
The Netherlands

George Halkias

Institute of Microelectronics,
NCSR “Demokritos”,
Greece

Eric Jalaguier

CEA-DRT-LETI,
17 rue des Martyrs,
38054 Grenoble,
France

Efstathios D. Kyriakis-Bitaros

Institute of Microelectronics,
NCSR "Demokritos",
Greece

Chrystelle Lagahe-Blanchard

CEA-LETI, Département
des Technologies Silicium,
17 rue des Martyrs,
38054 Grenoble Cedex 9,
France

Fabrice Letertre

Soitec S.A.
Parc Technologique des Fontaines
38926 Crolles cedex,
France

Miguel Levy

Physics Department, Michigan
Technological University,
Houghton, MI 49931
USA

Christophe Maleville

Soitec S A,
Parc Technologique des Fontaines,
38926 Crolles Cedex,
France

Andrew W. Nevin

Analog Devices Belfast Ltd.,
5 Hannahstown Hill, Belfast,
Northern Ireland, BT17 0LT,
United Kingdom

Susumu Noda

Kyoto University,
Japan

Matthias Petzold

Fraunhofer-Institut für
Werkstoffmechanik
06120 Halle,
Germany

Andreas Plöbl

OSRAM
Opto Semiconductors GmbH
Leibnizstraße 4
93055 Regensburg
Germany

Antonije M. Radojevic

DuPont Photonics Technologies,
Wilmington, MA 01887
USA

Ionut Radu

Max Planck Institute
of Microstructure Physics,
06120 Halle,
Germany

Kiyofumi Sakaguchi

ELTRAN Business Center,
Canon Inc.,
6770 Tamura, Hiratsuka,
Kanagawa,
Japan

Timothy D. Sands

School of Electrical and Computer
Engineering
Purdue University,
West Lafayette, IN
USA

Izabela Szafraniak

Poznan University of Technology,
Institute Of Materials Science
and Engineering,
M. Sklodowska-Curie 5 Sq.
60-965 Poznan
POLAND

H.-S. Philip Wong

IBM Research,
T.J. Watson Research Center,
Yorktown Heights,
New York 10598,
USA

William S. Wong

IBM Palo Alto Research Center,
Palo Alto, CA
USA

Takao Yonehara

ELTRAN Business Center,
Canon Inc.,
6770 Tamura, Hiratsuka,
Kanagawa,
Japan