

Part III

Wireless Sensor Nodes

The last part of this Ph.D. work describes the development of a wireless integrated RFID tag. It will be seen in Chap. 9 that the main building blocks of this tag have previously been developed in Part II. While discussing the architecture of the RFID tag, it will be shown that the architecture of the building blocks results in a greatly increased flexibility of the tag.

Finally, in the last chapter, Chap. 10, an elaborate comparison will be made between all oscillator implementations discussed in this Ph.D. work and the state of the art. Furthermore, the drawbacks of the different architectures will be compared, which finally results in suggestions for possible future work.