

Appendix

Loaded Transformer Input Impedance

Figure A.1a shows a possible representation of the transformer model, where a series resistance is added to the model in [Long00]. Figure A.1b shows the transformer model when loaded with a complex impedance.

The input impedance can be written as following:

$$\begin{aligned}
 Z_{in} &= (Z_{L_1} - X_M) + X_M // (Z_{L_2} - X_M + Z_L) \\
 &= Z_{L_1} - X_M + \frac{X_M(Z_{L_2} - X_M + Z_L)}{Z_{L_2} + Z_L} \\
 &= Z_{L_1} - \frac{X_M^2}{Z_{L_2} + Z_L}
 \end{aligned}
 \tag{A.1}$$

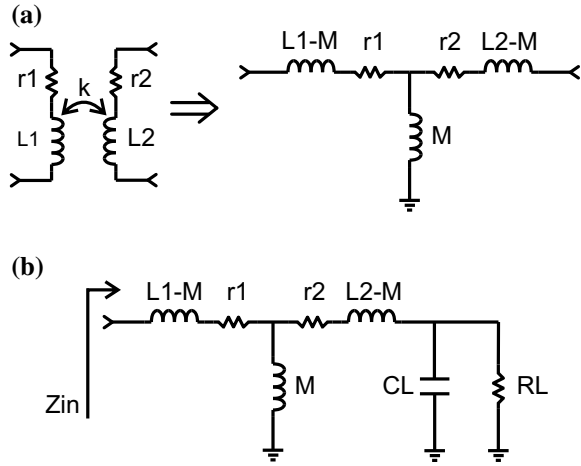
X_M in A.1 is $j\omega M$, where M is the mutual inductance, and can be written as:

$$M = k \times \sqrt{L_1 L_2}
 \tag{A.2}$$

Equation A.1 can then be written as:

$$Z_{in} = r_1 + j\omega L_1 + \frac{k^2 \omega^2 L_1 L_2}{r_2 + j\omega L_2 + \frac{R_L}{1 + j\omega C_L R_L}}
 \tag{A.3}$$

Fig. A.1 a Equivalent transformer model and b when loaded with a complex impedance



A.1 Real Part of the Input Impedance

The real part of Eq. A.3 can be written as following:

$$Real(Z_{in}) = r_1 + \frac{k^2 \omega^2 L_1 L_2 [r_2 + R_L (1 - \omega^2 L_2 C_L) + \omega^2 R_L C_L (L_2 + r_2 R_L C_L)]}{[r_2 + R_L (1 - \omega^2 L_2 C_L)]^2 + \omega^2 (L_2 + r_2 R_L C_L)^2} \quad (\text{A.4})$$

It can be shown that the real part of the input impedance can be written as:

$$Real(Z_{in}) = r_1 + \frac{k^2 \omega^2 L_1 L_2 \{R_L + r_2 [1 + (\omega R_L C_L)^2]\}}{r_2 \{2R_L + r_2 [1 + (\omega R_L C_L)^2]\} + R_L^2 (1 - \omega^2 L_2 C_L)^2 + (\omega L_2)^2} \quad (\text{A.5})$$

A.2 Imaginary Part of the Input Impedance

The imaginary part of Eq. A.3 can be written as following:

$$Imag(Z_{in}) = \omega L_1 + \frac{k^2 \omega^2 L_1 L_2 \{j\omega R_L C_L (r_2 + R_L - \omega^2 R_L C_L L_2) - j\omega (L_2 + r_2 R_L C_L)\}}{r_2 \{2R_L + r_2 [1 + (\omega R_L C_L)^2]\} + R_L^2 (1 - \omega^2 L_2 C_L)^2 + (\omega L_2)^2} \quad (\text{A.6})$$

It can be shown that the imaginary part of the input impedance can be written as:

$$\text{Imag}(Z_{in}) = \omega L_1 \left(1 + \frac{k^2 \omega^2 L_2 [R_L^2 C_L (1 - \omega^2 L_2 C_L) - L_2]}{r_2 \{ 2R_L + r_2 [1 + (\omega R_L C_L)^2] \} + R_L^2 (1 - \omega^2 L_2 C_L)^2 + (\omega L_2)^2} \right) \quad (\text{A.7})$$

A.3 Simplified Input Impedance Equations

If we assume an infinite quality factor, Eqs. A.5 and A.7 can be rewritten as following:

$$\text{Real}(Z_{in}) = \frac{k^2 \omega^2 L_1 L_2 R_L}{R_L^2 (1 - \omega^2 L_2 C_L)^2 + (\omega L_2)^2} \quad (\text{A.8})$$

$$\text{Imag}(Z_{in}) = \omega L_1 \left(1 + \frac{k^2 \omega^2 L_2 [R_L^2 C_L (1 - \omega^2 L_2 C_L) - L_2]}{R_L^2 (1 - \omega^2 L_2 C_L)^2 + (\omega L_2)^2} \right) \quad (\text{A.9})$$

If the load is only capacitive (i.e., R_L is approaching infinity), the equations become:

$$\text{Real}(Z_{in}) = \frac{k^2 \omega^2 L_1 L_2}{R_L (1 - \omega^2 L_2 C_L)^2} = 0 \quad (\text{A.10})$$

$$\text{Imag}(Z_{in}) = \omega L_1 \left(1 + \frac{k^2 \omega^2 L_2 C_L}{1 - \omega^2 L_2 C_L} \right) \quad (\text{A.11})$$

If the load is only resistive, Eqs. A.8 and A.9 become:

$$\text{Real}(Z_{in}) = \frac{k^2 \omega^2 L_1 L_2 R_L}{R_L^2 + (\omega L_2)^2} \quad (\text{A.12})$$

$$\text{Imag}(Z_{in}) = \omega L_1 \left(1 - \frac{k^2 (\omega L_2)^2}{R_L^2 + (\omega L_2)^2} \right) \quad (\text{A.13})$$

References

- [Agi13] Agilent application note: Wireless LAN at 60 GHz—IEEE 802.11ad explained. <http://cp.literature.agilent.com/litweb/pdf/5990-9697EN.pdf> (2013). Last accessed 14 Jan 2016
- [App] itunes store: Download times will vary. <https://support.apple.com/en-us/HT201587>. Last accessed 14 Jan 2016
- [Boers10] Boers, M.: A 60 GHz transformer coupled amplifier in 65 nm digital CMOS. In: Radio Frequency Integrated Circuits Symposium (RFIC), 2010 IEEE, pp. 343–346 (2010)
- [Bourdoux08a] Bourdoux, A., Nsenga, J., Thillo, W.V., Wambacq, P., der Perre, L.V.: Gbit/s radios @ 60 GHz: to OFDM or not to OFDM?. In: 2008 IEEE 10th International Symposium on Spread Spectrum Techniques and Applications, pp. 560–565 (2008)
- [Bourdoux08b] Bourdoux, A., Nsenga, J., Van Thillo, W., Wambacq, P., Van der Perre, L.: Gbit/s radios@ 60 GHz: to OFDM or not to OFDM? In: Spread Spectrum Techniques and Applications, 2008 IEEE 10th International Symposium on, pp. 560–565 (2008)
- [Brebels16] Brebels, S., Khalaf, K., Mangraviti, G., Vaesen, K., Libois, M., Parvais, B., Vidojkovic, V., Szortyka, V., Bourdoux, A., Wambacq, P., Soens, C., van Thillo, W.: 60-GHz CMOS TX/RX chipset on organic packages with integrated phased-array antennas. In: 2016 10th European Conference on Antennas and Propagation (EuCAP), pp. 1–5 (2016)
- [Chan09] Chan, W., Long, J., Spirito, M., Pekarik, J.: A 60GHz-band 1V 11.5 dbm power amplifier with 11% PAE in 65 nm CMOS. In: Solid-State Circuits Conference—Digest of Technical Papers, ISSCC 2009, IEEE International, pp. 380–381, 381a (2009)
- [Chan10] Chan, W., Long, J., Spirito, M., Pekarik, J.: A 60 GHz-band 2×2 phased-array transmitter in 65nm CMOS. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2010 IEEE International, pp. 42–43 (2010)
- [Chen13] Chen, J., Ye, L., Titz, D., Giancesello, F., Pilard, R., Cathelin, A., Ferrero, F., Luxey, C., Niknejadm, A.: A digitally modulated mm-wave cartesian beamforming transmitter with quadrature spatial combining. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2013 IEEE International, pp. 232–233 (2013)
- [Cis11] Cisco Visual Networking Index: Global mobile data traffic forecast update, 2010–2015 white paper. https://engineering.nd.edu/news-publications/pressreleases/Cisco_VNI_Global_Mobile_Data_Traffic_Forecast_2010_2015.pdf (2011). Last accessed 14 Jan 2016

- [Cis15] Cisco Visual Networking Index: Global mobile data traffic forecast update, 2014–2019 white paper. http://www.cisco.com/c/en/us/solutions/collateral/service-provider/visual-networking-index-vni/white_paper_c11-520862.pdf (2015). Last accessed 14 Jan 2016
- [Cripps02] Cripps, S.C.: *Advanced Techniques in RF Power Amplifier Design*. Artech House (2002)
- [Cripps06] Cripps, S.C.: *RF Power Amplifiers for Wireless Communications*, 2nd edn. Artech House (2006)
- [Emami11] Emami, S., Wiser, R., Ali, E., Forbes, M., Gordon, M., Guan, X., Lo, S., McElwee, P., Parker, J., Tani, J., Gilbert, J., Doan, C.: A 60 GHz CMOS phased-array transceiver pair for multi-gb/s wireless communications. In: *Solid-State Circuits Conference Digest of Technical Papers (ISSCC)*, 2011 IEEE International, pp. 164–166 (2011)
- [Eri12] Ericsson Mobility Report: On the pulse of the network society. <http://www.ericsson.com/res/docs/2012/mobility-report/ericsson-mobility-report-nov-2012.pdf> (2012). Last accessed 14 Jan 2016
- [Eri13] Ericsson Mobility Report: On the pulse of the network society. <http://www.ericsson.com/res/docs/2013/mobility-report/ericsson-mobility-report-nov-2013.pdf> (2013). Last accessed 14 Jan 2016
- [Eri14] Ericsson Mobility Report: On the pulse of the network society. <http://www.ericsson.com/res/docs/2014/mobility-report/ericsson-mobility-report-nov-2014.pdf> (2014). Last accessed 14 Jan 2016
- [Eri15] Ericsson Mobility Report: On the pulse of the network society. <http://www.ericsson.com/res/docs/2015/mobility-report/ericsson-mobility-report-nov-2015.pdf> (2015). Last accessed 14 Jan 2016
- [Gonzalez96] Gonzalez, G.: *Microwave Transistor Amplifiers: Analysis and Design*, 2nd edn. Upper Saddle River, NJ, USA: Prentice-Hall, Inc. (1996)
- [Gray09] Gray, P.R.: *Analysis and Design of Analog Integrated Circuits*, 5th edn. Wiley Publishing (2009)
- [IEE12] IEEE standard for information technology–telecommunications and information exchange between systems–local and metropolitan area networks–specific requirements–part 11: wireless LAN medium access control (MAC) and physical layer (PHY) specifications amendment 3: Enhancements for very high throughput in the 60 GHz band. IEEE Std 802.11ad-2012 (Amendment to IEEE Std 802.11-2012, as amended by IEEE Std 802.11ae-2012 and IEEE Std 802.11aa-2012), pp. 1–628 (2012)
- [Kaymaksut14] Kaymaksut, E., Zhao, D., Reynaert, P.: E-band transformer-based doherthy power amplifier in 40 nm CMOS. In: *Radio Frequency Integrated Circuits Symposium*, 2014 IEEE, pp. 167–170 (2014)
- [Khalaf13] Khalaf, K., Vidojkovic, V., Vaesen, K., Parvais, B., Long, J., Wambacq, P.: 60GHz transmitter front-end in 40nm LP-CMOS with improved back-off efficiency. In: *Silicon Monolithic Integrated Circuits in RF Systems (SiRF)*, 2013 IEEE 13th Topical Meeting on, pp. 6–8 (2013)
- [Khalaf14] Khalaf, K., Vidojkovic, V., Vaesen, K., Long, J., Van Thillo, W., Wambacq, P.: A digitally modulated 60GHz polar transmitter in 40nm CMOS. In: *Radio Frequency Integrated Circuits Symposium*, 2014 IEEE, pp. 159–162 (2014)
- [Khalaf15a] Khalaf, K., Vidojkovic, V., Long, J., Wambacq, P.: A 6x-oversampling 10gs/s 60GHz polar transmitter with 15.3% average pa efficiency in 40nm CMOS. In: *European Solid-State Circuits Conference (ESSCIRC)*, ESSCIRC 2015–41st, pp. 348–351 (2015)
- [Khalaf15b] Khalaf, K., Vidojkovic, V., Wambacq, P., Long, J.R.: *Data Transmission at Millimeter Waves*. Springer-Verlag, Berlin Heidelberg (2015)

- [Khalaf16] Khalaf, K., Vidojkovic, V., Vaesen, K., Libois, M., Mangraviti, G., Szortyka, V., Li, C., Verbruggen, B., Ingels, M., Bourdoux, A., Soens, C., Thillo, W.V., Long, J.R., Wambacq, P.: Digitally modulated cmos polar transmitters for highly-efficient mm-wave wireless communication. *IEEE J. Solid-State Circuits* **99**, 1–14 (2016)
- [Kulkarni14] Kulkarni, S., Reynaert, P.: 14.3 a push-pull mm-wave power amplifier with $<0.8^\circ$ AM-PM distortion in 40nm CMOS. In: *Solid-State Circuits Conference Digest of Technical Papers (ISSCC)*, 2014 IEEE International, pp. 252–253 (2014)
- [Kulkarni16] Kulkarni, S., Reynaert, P.: A 60-GHz power amplifier with AM-PM distortion cancellation in 40-nm CMOS. *IEEE Trans. Microw. Theory Tech.* **64**(7), 2284–2291 (2016)
- [Li15] Li, C., Li, M., Khalaf, K., Bourdoux, A., Verhelst, M., Ingels, M., Wambacq, P., Craninckx, J., Van Der Perre, L., Pollin, S.: Opportunities and challenges of digital signal processing in deeply technology-scaled transceivers. *J. Signal Process. Syst.*, **78**(1), 5–19 (2015). <http://dx.doi.org/10.1007/s11265-014-0940-x>
- [Long00] Long, J.: Monolithic transformers for silicon RF IC design. *IEEE J. Solid-State Circuits* **35**(9), 1368–1382 (2000)
- [Mangraviti12] Mangraviti, G., Parvais, B., Vidojkovic, V., Vaesen, K., Szortyka, V., Khalaf, K., Soens, C., Vandersteen, G., Wambacq, P.: A 52-66GHz subharmonically injection-locked quadrature oscillator with 10GHz locking range in 40nm LP CMOS. In: *Radio Frequency Integrated Circuits Symposium (RFIC)*, 2012 IEEE, pp. 309–312 (2012)
- [Markulic14] Markulic, N., Raczkowski, K., Wambacq, P., Craninckx, J.: A 10-bit, 550-fs step digital-to-time converter in 28nm CMOS. *European Solid State Circuits Conference (ESSCIRC)*, ESSCIRC 2014–40th. Sept 79–82 (2014)
- [Mitomo12] Mitomo, T., Tsutsumi, Y., Hoshino, H., Hosoya, M., Wang, T., Tsubouchi, Y., Tachibana, R., Sai, A., Kobayashi, Y., Kurose, D., Ito, T., Ban, K., Tandai, T., Tomizawa, T.: A 2-Gb/s throughput CMOS transceiver chipset with in-package antenna for 60-GHz short-range wireless communication. *IEEE J. Solid-State Circuits* **47**(12), 3160–3171 (2012)
- [Nazemi15] Nazemi, A., Hu, K., Catli, B., Cui, D., Singh, U., He, T., Huang, Z., Zhang, B., Momtaz, A., Cao, J.: 3.4 a 36Gb/s PAM4 transmitter using an 8b 18GS/S DAC in 28nm CMOS. In: *Solid-State Circuits Conference–(ISSCC)*, 2015 IEEE International, pp. 1–3 (2015)
- [Okada11] Okada, K., Matsushita, K., Bunsen, K., Murakami, R., Musa, A., Sato, T., Asada, H., Takayama, N., Li, N., Ito, S., Chaivipas, W., Minami, R., Matsuzawa, A.: A 60GHz 16qam/8psk/qpsk/bpsk direct-conversion transceiver for IEEE 802.15.3c. In: *Solid-State Circuits Conference Digest of Technical Papers (ISSCC)*, 2011 IEEE International, pp. 160–162 (2011)
- [OKM+13] Okada, K., Kondou, K., Miyahara, M., Shinagawa, M., Asada, H., Minami, R., Yamaguchi, T., Musa, A., Tsukui, Y., Asakura, Y., Tamonoki, S., Yamagishi, H., Hino, Y., Sato, T., Sakaguchi, H., Shimasaki, N., Ito, T., Takeuchi, Y., Li, N., Bu, Q., Murakami, R., Bunsen, K., Matsushita, K., Noda, M., Matsuzawa, A.: Full four-channel 6.3-Gb/s 60-GHz CMOS transceiver with low-power analog and digital baseband circuitry. *IEEE J. Solid-State Circuits* **48**(1), 46–65 (2013)
- [Olieman15] Olieman, E., Annema, A.-J., Nauta, B.: An interleaved full Nyquist high-speed DAC technique. *IEEE J. Solid-State Circuits* **50**(3), 704–713 (2015)
- [Parvais10] Parvais, B., Scheir, K., Vidojkovic, V., Vandebriel, R., Vandersteen, G., Soens, C., Wambacq, P.: A 40 nm LP CMOS PLL for high-speed mm-wave communication. In: *Proceedings of the ESSCIRC*, 2010, pp. 254–257 (2010)

- [Raczkowski12] Raczkowski, K., Mangraviti, G., Szortyka, V., Spagnolo, A., Parvais, B., Vandebriel, R., Vidojkovic, V., Soens, C., D'Amico, S., Wambacq, P.: A four-path 60GHz phased-array receiver with injection-locked LO, hybrid beamforming and analog baseband section in 90nm CMOS. In: Radio Frequency Integrated Circuits Symposium (RFIC), 2012 IEEE, pp. 431–434 (2012)
- [Ru15] Ru, J.Z., Palattella, C., Geraedts, P., Klumperink, E., Nauta, B.: A high-linearity digital-to-time converter technique: constant-slope charging. *IEEE J. Solid-State Circuits* **50**(6), 1412–1423 (2015)
- [Saito13] Saito, N., Tsukizawa, T., Shirakata, N., Morita, T., Tanaka, K., Sato, J., Morishita, Y., Kanemaru, M., Kitamura, R., Shima, T., Nakatani, T., Miyanaga, K., Urushihara, T., Yoshikawa, H., Sakamoto, T., Motozuka, H., Shirakawa, Y., Yosoku, N., Yamamoto, A., Shiozaki, R., Takinami, K.: A fully integrated 60-GHz CMOS transceiver chipset based on WiGig/IEEE 802.11ad with built-in self calibration for mobile usage. *IEEE J. Solid-State Circuits* **48**(12), 3146–3159 (2013)
- [Shi13] Shi, Q., Vaesen, K., Parvais, B., Mangraviti, G., Wambacq, P.: A 5469.3 GHz dual-band VCO with differential hybrid coupler for quadrature generation. In: Solid-State Circuits Conference (A-SSCC), 2013 IEEE Asian, pp. 325–328 (2013)
- [Siligaris11] Siligaris, A., Richard, O., Martineau, B., Mounet, C., Chaix, F., Ferragut, R., Dehos, C., Lanteri, J., Dussopt, L., Yamamoto, S., Pilard, R., Busson, P., Cathelin, A., Belot, D., Vincent, P.: A 65-nm CMOS fully integrated transceiver module for 60-GHz wireless HD applications. *IEEE J. Solid-State Circuits* **46**(12), 3005–3017 (2011)
- [Szortyka12] Szortyka, V., Raczkowski, K., Vandebriel, R., Kuijk, M., Wambacq, P.: Analog baseband beamformer for use in a phased-array 60 GHz transmitter. In: 2012 IEEE 12th Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems (SiRF), pp. 167–170 (2012)
- [Szortyka15] Szortyka, V., Raczkowski, K., Kuijk, M., Wambacq, P.: A wideband beamforming lowpass filter for 60 GHz phased-array receivers. *IEEE Trans. Circuits Syst. I Regul. Pap.* **62**(9), 2324–2333 (2015)
- [Tabesh11] Tabesh, M., Chen, J., Marcu, C., Kong, L., Kang, S., Alon, E., Niknejad, A.: A 65nm CMOS 4-element sub-34mw/element 60GHz phased-array transceiver. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2011 IEEE International, pp. 166–168 (2011)
- [VG10] Valdes-Garcia, A., Nicolson, S., Lai, J.-W., Natarajan, A., Chen, P.-Y., Reynolds, S., Zhan, J.-H., Kam, D., Liu, D., Floyd, B.: A fully integrated 16-element phased-array transmitter in sige bicmos for 60-GHz communications. *IEEE J. Solid-State Circuits* **45**(12), 2757–2773 (2010)
- [Vidojkovic12] Vidojkovic, V., Mangraviti, G., Khalaf, K., Szortyka, V., Vaesen, K., Van Thillo, W., Parvais, B., Libois, M., Thijs, S., Long, J., Soens, C., Wambacq, P.: A low-power 57-to-66GHz transceiver in 40nm LP CMOS with -17db EVM at 7Gb/s. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2012 IEEE International, pp. 268–270 (2012)
- [Vidojkovic13] Vidojkovic, V., Szortyka, V., Khalaf, K., Mangraviti, G., Brebels, S., Van Thillo, W., Vaesen, K., Parvais, B., Issakov, V., Libois, M., Matsuo, M., Long, J., Soens, C., Wambacq, P.: A low-power radio chipset in 40nm LP CMOS with beamforming for 60GHz high-data-rate wireless communication. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2013 IEEE International, pp. 236–237 (2013)
- [vZP07] van Zeijl Paul, T., Collados, M.: A digital envelope modulator for a WLAN OFDM polar transmitter in 90 nm CMOS. *IEEE J. Solid-State Circuits* **42**(10), 2204–2211 (2007)
- [Wika] 3GPP. <https://en.wikipedia.org/wiki/3GPP>. Last accessed 14 Jan 2016
- [Wikb] IEEE 802.11. https://en.wikipedia.org/wiki/IEEE_802.11. Last accessed 14 Jan 2016

- [Wikc] Mobile broadband. https://en.wikipedia.org/wiki/Mobile_broadband. Last accessed 14 Jan 2016
- [Wikd] Uncompressed video. https://en.wikipedia.org/wiki/Uncompressed_video. Last accessed 14 Jan 2016
- [Yujiri03] Yujiri, L., Shoucri, M., Moffa, P.: Passive millimeter wave imaging. *IEEE Microw. Mag.* **4**(3), 39–50 (2003)
- [Zhao12a] Zhao, D., Kulkarni, S., Reynaert, P.: A 60 GHz dual-mode power amplifier with 17.4 dbm output power and 29.3% PAE in 40-nm CMOS. In: Proceedings of the ESSCIRC (ESSCIRC), Sept 2012, pp. 337–340 (2012)
- [Zhao12b] Zhao, D., Kulkarni, S., Reynaert, P.: A 60GHz outphasing transmitter in 40nm CMOS with 15.6dbm output power. In: Solid-State Circuits Conference Digest of Technical Papers (ISSCC), 2012 IEEE International, pp. 170–172 (2012)
- [Zhao13] Zhao, D., Reynaert, P.: A 60-GHz dual-mode class AB power amplifier in 40-nm CMOS. *IEEE J. Solid-State Circuits* **48**(10), 2323–2337 (2013)