

Preventing CLT Attacks on Obfuscation with Linear Overhead

Rex Fernando^(✉), Peter M.R. Rasmussen, and Amit Sahai

Center for Encrypted Functionalities, UCLA, Los Angeles, USA
{rex,rasmussen,sahai}@cs.ucla.edu

Abstract. We describe a defense against zeroizing attacks on indistinguishability obfuscation (iO) over the CLT13 multilinear map construction that only causes an additive blowup in the size of the branching program. This defense even applies to the most recent extension of the attack by Coron *et al.* (PKC 2017), under which a much larger class of branching programs is vulnerable. To accomplish this, we describe an attack model for the current attacks on iO over CLT13 by distilling an essential common component of all previous attacks.

This essential component is a constraint on the function being obfuscated. We say the function needs to be *input partitionable*, meaning that the bits of the function’s input can be partitioned into somewhat independent subsets. This notion constitutes an attack model which we show captures all known attacks on obfuscation over CLT13. We find a way to thwart these attacks by requiring a “stamp” to be added to the input of every function. The stamp is a function of the original input and eliminates the possibility of finding the independent subsets of the input necessary for a zeroizing attack. We give three different constructions of such “stamping functions” and prove formally that they each prevent any input partition.

We also give details on how to instantiate one of the three functions efficiently in order to secure any branching program against this type of attack. The technique presented alters any branching program obfuscated over CLT13 to be secure against zeroizing attacks with only an additive blowup of the size of the branching program that is linear in the input size and security parameter.

We can also apply our defense to a recent extension of annihilation attacks by Chen *et al.* (EUROCRYPT 2017) on obfuscation over the GGH13 multilinear map construction.

Keywords: Obfuscation · Zeroizing attacks

Research supported in part from a DARPA/ARL SAFEWARE award, NSF Frontier Award 1413955, NSF grants 1619348, 1228984, 1136174, and 1065276, BSF grant 2012378, a Xerox Faculty Research Award, a Google Faculty Research Award, an equipment grant from Intel, and an Okawa Foundation Research Grant. This material is based upon work supported by the Defense Advanced Research Projects Agency through the ARL under Contract W911NF-15-C-0205. The views expressed are those of the authors and do not reflect the official policy or position of the Department of Defense, the National Science Foundation, or the U.S. Government.

1 Introduction

Indistinguishability obfuscation (iO) has so far relied on multilinear maps for instantiation (e.g. [GGH+13b]) and viable candidates for such are sparse. On top of that, the few that exist [GGH13a, CLT13, GGH15] have all been shown to suffer from significant vulnerabilities. However, not all attacks against these multilinear maps can be applied to iO. The very particular structure that most iO candidates induce puts numerous constraints on the way the encoded values can be combined, thus often not allowing the flexible treatment needed to mount an attack. Attacks on iO schemes have nonetheless been found for obfuscation of increasingly general families of functions.

In this paper, we focus on the Coron-Lepoint-Tibouchi (CLT13) multilinear maps [CLT13]. The known attacks over CLT13 are called *zeroizing attacks* [CHL+15, CGH+15, CLLT17]. To be carried out, they require multiple zero encodings that are the result of multiplications of elements that satisfy a certain structure. Since obfuscations of matrix branching programs only produce zeroes when evaluated in a very specific manner, setting up such a zeroizing attack on an obfuscated branching program is rather non-trivial.

Because of this, the first paper applying zeroizing attacks over CLT13 to iO only showed how to apply the attack to very simple branching programs [CGH+15], and attacking more realistic targets seemed out of reach of this technique. However, a very recent work by Coron et al. [CLLT17] introduced a simple method that can transform a much larger class of branching programs into ones that have this very specific structure. As such, zeroizing attacks appear much more threatening to the security of iO over CLT13 than previously thought.

1.1 The Story So Far: Branching Programs and Zeroizing Attacks

This section will serve as a light introduction to the terminology and concepts at work in this paper.

Branching Programs. The “traditional” method of obfuscation works with matrix branching programs that encode boolean functions. A (single input) matrix branching program BP is specified by the following information. It has a length ℓ , input size n , input function $\mathbf{inp}: [\ell] \rightarrow [n]$, square matrices $\{A_{i,b}\}_{i \in [\ell], b \in \{0,1\}}$, and bookend vectors A_0 and $A_{\ell+1}$. An evaluation of the branching program BP on input $x \in \{0,1\}^n$ is carried out by computing

$$A_0 \times \prod_{i=1}^{\ell} A_{i, x_{\mathbf{inp}(i)}} \times A_{\ell+1}.$$

If the product is zero then $\mathbf{BP}(x) = 0$ and otherwise, $\mathbf{BP}(x) = 1$.

Multilinear Maps and Obfuscating Branching Programs. Current instantiations of iO are based on graded multilinear maps [GGH+13b, BGK+14]. This primitive allows values $\{a_i\}$ to be encoded to $\{[a_i]\}$ in such a manner that they are hidden. The multilinear map allows evaluation of a very restricted class of polynomials over these encoded values. Moreover, evaluating a polynomial, p , over the encodings in this way should only yield one bit of information: whether or not the result, $p(\{a_i\})$, is zero.

To obfuscate a branching program, we first randomize the matrices and then encode the entries of the matrix using a multilinear map. (See for example [BGK+14] for details on how the matrices are randomized.) The hope is that the multilinear map will allow evaluations of the branching program but will not allow other malicious polynomials over the encodings that would violate indistinguishability. In fact, Barak *et al.* [BGK+14] show that if zero testing the result of evaluations over the multilinear map truly only reveals whether or not the evaluation is zero and does not leak anything else then this scheme is provably secure.

Zeroizing Attacks on Obfuscated Branching Programs. Unfortunately, the assumption that zero-testing does not leak any information is unrealistic. In particular, the zeroizing attacks over the CLT13 multilinear map work by exploiting the information leaked during successful zero tests to obtain the secret parameters.

Before discussing the zeroizing attacks on iO, we first consider how the attacks work over raw encodings. Each of the known zeroizing attacks requires sets of encodings that satisfy a certain structure. We describe here the structure required for the simplest attack. This version of the attack was first presented in [CHL+15]. Namely, to attack a CLT instance of dimension n , an adversary needs three sets of encodings $\{B_i\}_{i \in [n]}$, $\{C_0, C_1\}$, and $\{D_j\}_{j \in [n]}$ such that for every $i, j \in [n]$ and $\sigma \in \{0, 1\}$, $B_i C_\sigma D_j$ is a top-level encoding of zero. In other words, we must be able to vary the choice of encoding in each set independently of the other choices and always produce an encoding of zero. If an adversary is able to obtain such sets, then the adversary is able to factor the modulus of the ciphertext ring, completely breaking the CLT instance. There are several variants of this attack, some requiring sets of vectors or matrices of encodings instead of plain encodings. But all have the requirement of obtaining three sets which we can combine in some way to achieve encodings of zero, and where we can vary the choice from each set independently of the others. We give more details about these attacks in Sect. 3.

We show (Theorem 2, Sect. 3) that all currently known zeroizing attacks over CLT13 to branching program obfuscation give rise to a constraint on the function being obfuscated, which we call an *input partition*, described below. In fact, Theorem 2 shows that this applies not only to all current zeroizing attacks over CLT13 but to a broader class of zeroizing attacks, as we discuss further below.

Input Partitioning. Let $f: \{0, 1\}^n \rightarrow \{0, 1\}$ be a function to be obfuscated. We say that there is an *input partitioning* of f if there exist sets $A \subseteq \{0, 1\}^k, B \subseteq \{0, 1\}^l, k + l = n$ and a permutation $\pi \in S_n$ such that $|A|, |B| > 1$ and for every $a \in A$ and $b \in B$, $f(\pi(a||b)) = 0$, where π acts on the bit-string $a||b$ by permuting its bits. In words, the function f can be input partitioned if the indices of the input can be partitioned into two sets such that varying the bits of the partitions independently within certain configurations will always yield zero as the output.

All current attacks on obfuscation over CLT13 require an input partition, and we will describe later how there is strong evidence that more generally any zeroizing attack will also require it. In fact, the current attacks need a partition into three parts to succeed, but since any input partition into three parts can be viewed as an input partition into two parts, we treat that more general case instead.

It is worth noting that zeroizing attacks, in fact, require a stronger condition on the branching program in order to succeed; the matrices of the obfuscated branching program must be organized in a specific way in relation to the three sets of inputs. But preventing an input partition necessarily prevents this stronger condition.

1.2 Our Contributions

Our aim in this paper is to provide a robust defense against the known classes of zeroizing attacks for iO over CLT13 and against potential future extensions of these attacks. Further, we want the defense to have a minimal impact on the efficiency of the obfuscated program. In this section, we describe how we achieve such a defense that only incurs an additive linear blowup in the multilinearity.¹

Attack Model. Our defense is built on an attack model based on the input partitions described above. Previous authors [CGH+15, CLLT17] have considered the stronger condition mentioned at the end of the section on input partitioning above as a requirement for their attacks, but we are the first to consider the input partition of a boolean function as the basis of a formal attack model.

In Sect. 4.1 we define this attack model formally. Before this, we show in Sect. 3.3 that the model captures all current zeroizing attacks on obfuscation over CLT13. We also argue that the model broadly captures any new attack which uses the general strategy of these attacks.

There is a simple intuition behind why the attack model is sufficient for capturing any zeroizing attack on obfuscation over CLT13. Obfuscation schemes are designed so that the ways in which an adversary can construct encodings of zero are severely restricted. Intuitively, the sets of polynomials over the encodings that an adversary should be allowed to compute should only be honest evaluations of branching programs, or something very close to this. In fact, [BMSZ16]

¹ An important practical caveat is that we will work with the variant of CLT13 described in [GLW14], which avoids a vulnerability by increasing the dimension of the CLT13 instance. We give more details on this in Sect. 5.3.

prove that the standard obfuscation scheme used in this paper has a property very close to this intuition, namely that the only successful evaluations of polynomials of the encodings are given by linear combinations of honest evaluations of the obfuscated program. Recall that zeroizing attacks over CLT13 require three sets of encodings; the result of [BMSZ16] shows the adversary has very few degrees of freedom in constructing the three sets other than varying the inputs to the branching program.

Given this model, we construct a procedure which takes an input partitionable function $f: \{0, 1\}^n \rightarrow \{0, 1\}$ and produces a function g with the same functionality, but on which no input partitions exist. The existence of an input partition depends on which inputs cause g to output zero; note that a branching program is defined to output zero if the result of the multiplications of the matrices is zero and one if the result is any other value. So we will think of g as being a function $\{0, 1\}^{n'} \rightarrow \{0, \perp\}$.

Input Stamping. The idea behind such a procedure is to append a “stamp” to the end of the input of the function f . The stamp is designed to not allow the input as a whole to be input partitioned. More specifically, we will construct a function $h: \{0, 1\}^n \rightarrow \{0, 1\}^m$ such that given any $f: \{0, 1\}^n \rightarrow \{0, 1\}$ we can construct a new program $g: \{0, 1\}^{n+m} \rightarrow \{0, \perp\}$ from f such that $g(s)$ outputs 0 if and only if the input is of the form $s = x||h(x)$ and $f(x) = 0$ and otherwise outputs \perp . Note that the original $\{0, 1\}$ -output of f is recoverable from g as long as the evaluation took place with the correct stamp appended to the input.

Our main theoretical result is to find a necessary and sufficient condition on h such that g cannot be input partitioned. If this is the case then we say h secures f . We state a sufficient condition below as Theorem 1. In Sect. 4.3 we restate Theorem 1 with both necessary and sufficient conditions after introducing some preliminaries which are required for the stronger version of the theorem.

Theorem 1 (Weakened). *Let $h: \{0, 1\}^n \rightarrow \{0, 1\}^m$. Let $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \{0, 1\}^n$ be treated as integers. If whenever*

$$\begin{aligned} x_{1,1} - x_{1,2} &= x_{2,1} - x_{2,2} \\ h(x_{1,1}) - h(x_{1,2}) &= h(x_{2,1}) - h(x_{2,2}) \end{aligned}$$

it is the case that $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$, then h secures all functions $f: \{0, 1\}^n \rightarrow \{0, 1\}$.

With this theorem, two questions arise: whether is feasible to construct such an h , and how efficiently we can construct the modified g to be. The second question is relevant with respect to the work in [AGIS14, BISW17] on improving the efficiency of obfuscation of branching programs. It is especially relevant to [BISW17] since this paper uses CLT13 to achieve a significant speedup factor from previous constructions. Thus, establishing the security of obfuscation over CLT13 with minimal overhead is pertinent.

With regards to efficiency, the size of the image of h becomes important. Using an h that has an output size m which is large relative to n will necessarily

affect the efficiency of the resulting g . In Sect. 4.4 we explore the minimum value of m necessary in order for h to be secure. We show that m must be at least linear in terms of n .

Constructions. The first two instantiations we present for h address the question of the feasibility of constructing such a function. They are both number-theoretic and follow from the fact that Theorem 1 can be interpreted as a sort of nonlinearity property. We show that squaring and exponentiation modulo a large enough prime satisfy this property and thus secure any function f . We stress that we do not rely on any number-theoretic assumptions in the proofs that these functions satisfy Theorem 1.

The third instantiation is a combinatorial function and is motivated by the desire for efficiency. To that end, instead of defining a single function which is guaranteed to have the property specified above, we define a family of very simple functions where the probability of a random choice from this family is very likely to have the property.

We define h as follows. Let k and t be parameters set beforehand. For each $i \in [t]$ and $j \in [n]$, choose $\pi_{i,j,0}$ and $\pi_{i,j,1}$ at random from the set of permutations acting on k elements. For an input $x \in \{0, 1\}^n$, define

$$h_i(x) = (\pi_{i,1,x_1} \circ \pi_{i,2,x_2} \circ \dots \circ \pi_{i,n,x_n})(1).$$

Then $h(x) = h_1(x) \parallel h_2(x) \parallel \dots \parallel h_t(x)$.

We give a combinatorial probabilistic argument that with $k = O(1)$ and $t = O(n + \lambda)$ the choice of h secures all functions $f: \{0, 1\}^n \rightarrow \{0, 1\}$ with overwhelming probability as a function of λ .

Parallel Initialization. Since this construction for h is defined in terms of permutations and processes the input in the same way that a branching program does, constructing a branching program that computes such an h and subsequently modifying a branching program for f to create a branching program for the corresponding g is fairly straightforward. While this is already vastly more efficient than implementing the first two instantiations of h using a matrix branching program, running the functions h_i in sequence would cause a linear blowup in the size of the branching program. We do much better than this and achieve a constant blowup factor with the following trick. Unlike the GGH13 multilinear map construction, CLT13 allows for a composite ring size. We use this fact to evaluate all the h_i in parallel. This achieves a constant factor overhead in the branching program size.² (This technique was used, for example, in [AS17, GLSW15], albeit for different purposes.)

Perspectives. We remark that the attacks in [CLLT17] still do not apply to obfuscations of all branching programs. Specifically, if the branching program

² In fact, our actual overhead is additive and linear in terms of the input size of f , not the size of its branching program. See Sect. 5.3 for details.

is too long compared to the input size then there is a blowup associated with the transformation in [CLLT17] which becomes infeasible. Also, it is not yet known how to apply the attack to dual-input branching programs, due to a similar blowup in complexity. Although this is the case, it is definitely possible that future work will extend zeroizing attacks to longer branching programs and dual-input branching programs. Our defense hedges against these possible future attacks, because it defends against any attack which requires an input partition.

It is noteworthy to contrast this line of work with the recent attacks on iO over the GGH13 [GGH13a] multilinear maps construction. In [MSZ16] Miles et al. implement the first known such attacks, which they call *annihilating attacks*. A follow-up paper [GMM+16] gives a weakened multilinear map model and an obfuscation construction in this model which is safe against all annihilating attacks. We stress that the attacks over CLT13 are not related to these annihilating attacks, which are not known to work over CLT13. However, a recent paper attacking obfuscation over GGH13 [CGH17], in which the authors extend annihilating attacks to the original GGHRSW construction of iO, does use an input partition as part of their attack. They do this as a first step in order to recover a basis for the ideal which defines the plaintext space. Our defense applies to this step of their attack.

As a final note, our defense does not operate in a weak multilinear map model, in contrast to the one defined in [GMM+16]. We leave it as an important open question to develop such a weak multilinear map model for CLT13.

Organization. In Sect. 3 we discuss the attacks on obfuscation over CLT13 in more detail. In Sect. 4 we define formally what it means for a function to be input partitionable, and give a necessary and sufficient condition for any h to secure a function. We also give our lower bound on the size of the image of h . Finally, in Sect. 5 we define and prove the correctness of our instantiations of h .

2 Notation

We first introduce some notation for our exposition.

Definition 1. For any positive integer $k \in \mathbb{N}$ we denote by \mathbb{Z}_k the set $\mathbb{Z}/k\mathbb{Z}$.

Definition 2. Let $n \in \mathbb{N}$ be a positive integer and $\mathbf{v} \in \mathbb{N}^n$ a vector. We will denote by $\mathbb{Z}_{\mathbf{v}}$ the set

$$\mathbb{Z}_{v_1} \times \mathbb{Z}_{v_2} \times \cdots \times \mathbb{Z}_{v_n}.$$

In this and following sections we will consider functions f that we want to secure and input stamping functions h . We will consider such functions as having domains and/or codomains of the form $\mathbb{Z}_{\mathbf{v}}$. Note that if we define $\mathbf{v} = (2, 2, \dots, 2)$, then $\mathbb{Z}_{\mathbf{v}} = \{0, 1\}^n$, so this is a generalization of binary functions. We do this because in the instantiations section we will define an h which needs this generalized input format. Thus we state all theorems using this more general format to accommodate such instantiations.

Definition 3. For a positive integer $n \in \mathbb{N}$ we denote by $[n]$ the set $\{1, 2, \dots, n\}$.

Definition 4. For a positive integer $t \in \mathbb{N}$, we denote by S_t the set of permutations of the set $[t]$.

Definition 5. For two vectors or strings a and b let $a||b$ denote their concatenation.

3 Attack Model for Zeroizing Attacks on Obfuscation

In this section, we give a high-level overview of the new attack by Coron, Lee, Lepoint and Tibouchi [CLLT17]. We start by reviewing the older attacks in [CHL+15, CGH+15] which this attack is based on.

3.1 CLT13 Zeroizing Attacks

The basic idea behind all the zeroizing attacks over CLT13 is to exploit the specific structure of the zero-test of CLT13, which differs from the other multilinear map constructions. CLT13 works over a ring $\mathbb{Z}_{x_0} \cong \bigoplus_{i=1}^k \mathbb{Z}_{p_i}$ where each p_i is a large prime and zero-testing of an encoding works by multiplying the encoding by a zero-test parameter \mathbf{p}_{zt} , checking that the result is “small” in \mathbb{Z}_{x_0} . The simplest version of the attack adheres to the following outline: the adversary finds three sets of CLT13 encodings $\{a_i\}_{i \in [n]}$, $\{b_0, b_1\}$, and $\{c_j\}_{j \in [n]}$ such that for every $i, j \in [n]$ and $\sigma \in \{0, 1\}$, $a_i b_\sigma c_j$ is a top-level encoding of zero. Now, define the matrices $W_\sigma, \sigma \in \{0, 1\}$ by

$$W_\sigma[i, j] = \mathbf{p}_{zt} a_i b_\sigma c_j$$

where $W_\sigma[i, j]$ denotes the entry of W in the i th row and j th column. Since each W_σ is an outer product of elements of \mathbb{Z}_{x_0} it can never be full-rank over \mathbb{Z}_{x_0} . A key point in the zeroizing attacks, however, is that the way the zero-test works, the matrix W_σ will be invertible over \mathbb{Q} when the right encodings $\{a_i\}_{i \in [n]}$, $\{b_0, b_1\}$, and $\{c_j\}_{j \in [n]}$ are chosen. Write b_σ as its decomposition in \mathbb{Z}_{x_0} by the Chinese Remainder Theorem, $b_\sigma = (b_{\sigma,1}, \dots, b_{\sigma,k})$, where for each i , $b_{\sigma,i} \equiv b_\sigma \pmod{p_i}$. Then computing $W_0 W_1^{-1}$ and finding the eigenvalues (over the rationals), one obtains the rational ratios $b_{0,i}/b_{1,i}$ for each i . This leads to a factorization of x_0 .

More general attacks allow that the encodings $\{a_i\}_{i \in [n]}$, $\{b_0, b_1\}$, and $\{c_j\}_{j \in [n]}$ be replaced with matrices of encodings $\{A_i\}_{i \in [n]}$, $\{B_0, B_1\}$, $\{C_j\}_{j \in [n]} \in \mathbb{Z}_{x_0}^{d \times d}$ together with vectors $s \in \mathbb{Z}_{x_0}^{d \times 1}, t \in \mathbb{Z}_{x_0}^{1 \times d}$. In this case it is required that for every $i, j \in [n], \sigma \in \{0, 1\}$, $s \times A_i \times B_\sigma \times C_j \times t$ is an encoding of zero and the matrices W_0, W_1 are defined by

$$W_\sigma[i, j] = \mathbf{p}_{zt}(s \times A_i \times B_\sigma \times C_j \times t).$$

We again require that W_1 is invertible. Write B_σ as its decomposition in \mathbb{Z}_{x_0} by the Chinese Remainder Theorem $B_\sigma = (B_{\sigma,1}, \dots, B_{\sigma,k})$ where each $B_{\sigma,i}$ is a

matrix. Analyzing the characteristic polynomial of $W_0W_1^{-1}$ now yields the characteristic polynomials of $B_{0,i}B_{1,i}^{-1}$ for every i . This again leads to a factorization of x_0 .

We refer to [CGH+15] for further details.

3.2 Zeroizing Attacks on Obfuscation over CLT13

All known attacks on obfuscation over CLT13 have proceeded in a very similar manner to the method just described: since the evaluation of a branching program is a product of matrices over encodings in a multilinear map, they divide the steps of the branching program into three parts corresponding to the sets of encodings above such that these three parts can be varied independently of the others.

To see what we mean by this, let

$$M(x) = \widehat{M}_0 \times \prod_{i=1}^r \widehat{M}_{i,x_{\text{inp}(i)}} \times \widehat{M}_{r+1}, x \in \{0,1\}^t$$

be an obfuscation of a matrix branching program. We try to find $B_x = \widehat{M}_0 \times \prod_{i=1}^a \widehat{M}_{i,x_{\text{inp}(i)}}$, $C_x = \prod_{i=a+1}^b \widehat{M}_{i,x_{\text{inp}(i)}}$, and $D_x = \prod_{i=b+1}^r \widehat{M}_{i,x_{\text{inp}(i)}} \times M_{i,r+1}$ such that we can partition the input bits as $\mathcal{B} \cup \mathcal{C} \cup \mathcal{D} = [t]$ and the value of B_x , C_x , and D_x rely only on \mathcal{B} , \mathcal{C} , and \mathcal{D} , respectively. Write $M(bcd)$ to mean the evaluation of M where b specifies the bits with positions in \mathcal{B} , and with c, d likewise with \mathcal{C}, \mathcal{D} . We further try to find sets of bit strings $\mathcal{B}, \mathcal{C}, \mathcal{D}$ where \mathcal{B}, \mathcal{D} are large and \mathcal{C} is at least of size two and for all $b \in \mathcal{B}, c \in \mathcal{C}, d \in \mathcal{D}$, $M(bcd) = 0$. If we can do all this, then the corresponding products of matrices form products of zero which decompose in a similar manner to the products of encodings used for the previous attack, and can similarly be used to mount an attack on the CLT13 instance used.

The problem with using this attack directly is that only the very simplest of branching programs can be decomposed in this way. The three pieces of the branching program B_x, C_x and D_x which correspond to the three sets of encodings in the zeroizing attack must be consecutively arranged in the branching program. In particular, this rules out attacks on any branching program that makes several passes over its input.

The modified attack in [CLLT17] overcomes this limitation with a matrix identity which allows a rearranging of the matrix product corresponding to a branching program execution. The identity is as follows:

$$\mathbf{vec}(A \cdot B \cdot C) = (C^T \otimes A) \cdot \mathbf{vec}(B),$$

where \mathbf{vec} is the function sending a matrix $D = [d_1, d_2, \dots, d_n]$ for column vectors d_i to the vector

$$\mathbf{vec}(D) = \begin{bmatrix} d_1 \\ d_2 \\ \vdots \\ d_n \end{bmatrix}.$$

Using this identity, [CLLT17] shows how to attack a branching program with input function $\mathbf{inp}(i) = \min(i, 2t + 1 - i)$ for $1 \leq i \leq 2t + 1$. Note that any branching program with this function does not satisfy the property above which was required for the earlier CLT13 attacks, since every input bit except for the t -th bit controls two nonconsecutive positions in the branching program. This input function was originally used in the branching programs which were attacked in [MSZ16]. We can write such a program evaluation as

$$A(x) = B(x)C(x)D(x)C'(x)B'(x) \times p_{zt} \pmod{x_0}$$

where $B(x)$ and $B'(x)$ are both controlled by the same inputs, and likewise for $C(x)$ and $C'(x)$. [CLLT17] show that this can be rewritten as

$$(B'(x)^T \otimes B(x)) \times (C'(x)^T \otimes C(x)) \times \mathbf{vec}(D(x)) \times p_{zt} \pmod{x_0}$$

where now the three sets of inputs control consecutive pieces of the product. They then show how to use a modification of the original attack on this product, factoring x_0 .

3.3 Attack Model

To defend against zeroizing attacks as described above and other attacks following a similar tangent, we distil an attack model. The model we will be employing is fairly general and introduces the notion of an input partition, which will be formally defined in the following section. We here bring an informal version of the definition. Note that this definition only deals with functions with binary strings as input.

Definition 6 (Input Partition – Informal). *Let $f: \{0, 1\}^n \rightarrow \{0, \perp\}$ be a function. An input partition for f is a tuple*

$$(\sigma \in S_n; a_1, a_2 \in \{0, 1\}^{n_1}; c_1, c_2 \in \{0, 1\}^{n_2})$$

such that $n_1 + n_2 = n$ and for every choice of $i, j \in \{1, 2\}$,

$$f(\sigma(a_i || c_j)) = 0,$$

where σ permutes the string $a_i || c_j$ by permuting its indices.

Intuitively, the above definition describes that the input bits of the function f can be partitioned into two parts that can be varied independently while f still evaluates to 0. In the following we will demonstrate that the currently known

zeroizing attacks against obfuscation and the most natural derivatives of such all require an input partition of the obfuscated function to exist.

Now, suppose that we were given an obfuscated branching program

$$M(x) = \widehat{M}_0 \times \prod_{i=1}^r \widehat{M}_{i, x_{\text{inp}(i)}} \times \widehat{M}_{r+1}, x \in \{0, 1\}^t$$

as above, which we wish to use the technique of zeroizing attacks against. We will think of the entries of the matrices $\widehat{M}_0, \{\widehat{M}_{i,b}\}_{i \in [r]}, \widehat{M}_{r+1}$ as indeterminates since they are given to us as encoded values. Thus, an encoding from one of the matrices will uniquely identify that matrix. We need to find the partitioned set of encodings which is necessary to perform a zeroizing attack using the encodings of the branching program. We elaborate as follows. We are given CLT13 encodings for each element of the matrices $\widehat{M}_{i,b}$ and the vectors \widehat{M}_0 and \widehat{M}_{r+1} . We need to combine these in some way to obtain encodings a_i^s, b_σ^s, c_j^s indexed by $i, j \in [n], \sigma \in \{0, 1\}$, and $s \in I$ for an index set I such that for every choice of $i, j, \sigma, \sum_{s \in I} a_i^s b_\sigma^s c_j^s$ is an encoding of zero. Note that the matrix products of the more general attack can also be written out in this manner.

In every known zeroizing attack against an obfuscated program M , the zeroizing attack employed is such that $\sum_{s \in I} a_i^s b_\sigma^s c_j^s$ is an evaluation of M at a point $x^{i,\sigma,j} \in \{0, 1\}^n$. Specifically, the index set I and the a_i^s, b_σ^s, c_j^s are such that the a_i^s, b_σ^s, c_j^s correspond to entries in the $M_{i,b}$ of the branching program and

$$M(x^{i,\sigma,j}) = \widehat{M}_0 \times \prod_{l=1}^r \widehat{M}_{l, x_{\text{inp}(l)}^{i,\sigma,j}} \times \widehat{M}_{r+1} = \sum_{s \in I} a_i^s b_\sigma^s c_j^s.$$

This is even the case with the new attacks in [CLLT17]; they show that each evaluation can be *conceptually* rewritten as some other product, but they still use plain evaluations of a branching program.

Setting each element in the zeroizing attack matrix to be a branching program evaluation is obviously the most natural way to try to apply zeroizing attacks to obfuscations of branching programs, and there is strong evidence that it is the only way. This is because [BMSZ16] show that given an obfuscated branching program encoded in a multilinear map the only way to obtain a top-level zero is by taking a linear combination of honest branching program evaluations over inputs that evaluate to zero. In other words, for any zeroizing attack $\{a_i^s\}_{i,s}, \{b_\sigma^s\}_{\sigma,s}, \{c_j^s\}_{j,s}, i, j \in [n], \sigma \in \{0, 1\}, s \in I$, we have that for all i, j, σ ,

$$\sum_{s \in I} \alpha_s a_i^s b_\sigma^s c_j^s = \sum_{x \in \mathcal{X}_{i,\sigma,j}} \alpha_x M(x)$$

This is nearly the condition in Theorem 2. In all current attacks, in fact, only a single $M(x)$ is computed for each i, σ, j , and so we restrict our analysis to this setting.

We denote by f_x the zero-tested encoding $\mathbf{p}_{zt} \cdot M(x)$. In our analysis, we make the following simplifying assumptions about the encodings of a zeroizing attack. We stress that these assumptions are in line with every known zeroizing attack on obfuscation.

Assumption 1: Suppose that $\sum_{s \in I} a_i^s b_\sigma^s c_j^s = M(x)$ and define the set

$$\mathcal{E} = \{d \mid \exists i \text{ such that } d \text{ occurs in } \widehat{M}_{i, \text{inp}(i)}\}.$$

We assume that for every $s \in I$, a_i^s, b_σ^s , and c_j^s is a product of encodings from \mathcal{E} .

Assumption 2: The sum $\sum_{s \in I} a_i^s b_\sigma^s c_j^s$ is equal to the evaluation of M on a single input x .

We now show that all zeroizing attacks which follow this pattern must yield an input partition in the function being obfuscated, which constitutes a strong justification for our model’s usefulness.

Theorem 2. *Let a valid zeroizing attack against an obfuscated program M using the CLT13 encodings*

$$\{a_i^s\}_{i,s}, \{b_\sigma^s\}_{\sigma,s}, \{c_j^s\}_{j,s}, i, j \in [n], \sigma \in \{0, 1\}, s \in I$$

be given. Assume that the zeroizing attack satisfies Assumptions 1 and 2 and that for a family of inputs $\{x^{i,\sigma,j}\}$,

$$\sum_{s \in I} a_i^s b_\sigma^s c_j^s = M(x^{i,\sigma,j})$$

Then there must exist an input partition for the function encoded by the branching program.

Proof. For each encoding d of the obfuscated branching program M , define the *origin* of d_i as $\mathbf{or}(d) = (i, b)$ where $M_{i,b}$ is the matrix in which d occurs. For a product $\prod_{i=1}^k d_i$ of encodings, denote by $\mathbf{input} \left(\prod_{i=1}^k d_i \right)$ the set

$$\mathbf{input} \left(\prod_{i=1}^k d_i \right) = \bigcup_{i=1}^k \{(\mathbf{inp}(i), b) \mid \mathbf{or}(d_i) = (i, b)\}.$$

In words, considering $\prod_{i=1}^k d_i$ as part of an evaluation of the obfuscated branching program on some input x , $\mathbf{input} \left(\prod_{i=1}^k d_i \right)$ specifies which bits of x are determined to be what by the product. I.e. if $(i, b) \in \mathbf{input} \left(\prod_{i=1}^k d_i \right)$ then somewhere in the (partial) evaluation of the branching program, $x_i = b$ was used to determine the choice of matrix.

Now, assume for contradiction that we have a valid zeroizing attack as above for an obfuscated program M that has no input partition. We have that for every $i, j \in [n], \sigma \in \{0, 1\}$,

$$\sum_{s \in I} a_i^s b_\sigma^s c_j^s = M(x^{i, \sigma, j}).$$

Since every monomial $a_i^s b_\sigma^s c_j^s$ must produce a top-level encoding and because of Assumption 1,

$$\mathbf{input}(a_i^s) \cup \mathbf{input}(b_\sigma^s) \cup \mathbf{input}(c_j^s) = \{(k, x_k^{i, \sigma, j}) \mid k \in [t]\}$$

Define the sets

$$\begin{aligned} A &= \{\mathbf{input}(a_i^s) \mid i \in [n], s \in I\}, \\ B &= \{\mathbf{input}(b_\sigma^s) \mid \sigma \in \{0, 1\}, s \in I\}, \\ C &= \{\mathbf{input}(c_j^s) \mid j \in [n], s \in I\}. \end{aligned}$$

We say that A (resp. B, C) contains a *switch of input* if there exists $k \in [t]$ such that $\{(k, 0), (k, 1)\} \in A$ (resp. $\{(k, 0), (k, 1)\} \in B, \{(k, 0), (k, 1)\} \in C$). In that case we say that k is a bit position of A that *switches*. If A contains a switch of input, $\{(k, 0), (k, 1)\} \in A$, it means that there exists $i_1, i_2 \in [n]$ such that $x^{i_1, \sigma, j}$ and $x^{i_2, \sigma, j}$ differ in bit k . Note that since $\{(k, x_k^{i, \sigma, j}) \mid k \in [t]\}$ never contains a switch of input, it must be the case that $B \cap \{(k, 0), (k, 1)\} = \emptyset$ and $C \cap \{(k, 0), (k, 1)\} = \emptyset$ as for every s ,

$$\begin{aligned} \mathbf{input}(b_\sigma^s) \cup \mathbf{input}(c_j^s) &\subseteq \{(k, x_k^{i_1, \sigma, j}) \mid k \in [t]\} \\ \mathbf{input}(b_\sigma^s) \cup \mathbf{input}(c_j^s) &\subseteq \{(k, x_k^{i_2, \sigma, j}) \mid k \in [t]\} \end{aligned}$$

and the two sets on the right of the inclusions differ in bit k while the two sets on the left of the inclusions are the same.

Assume for contradiction that any two of A, B, C contain a switch of input and assume without loss of generality that it is A and B . Let k_1, \dots, k_{m_1} be the bit positions of A that switch and l_1, \dots, l_{m_2} be the bit positions of B that switch. Then by the above argument, there is no pair (k_s, b) contained in $B \cup C$ and no pair (l_s, b) contained in $A \cup C$. Let i_1, i_2 be given such that $x^{i_1, \sigma, j}$ and $x^{i_2, \sigma, j}$ differ in some bits and similarly let j_1, j_2 be such that x^{i, σ, j_1} and x^{i, σ, j_2} differ in some bits. Then there is an input partition given by the $x^{i_1, \sigma, j_1}, x^{i_1, \sigma, j_2}, x^{i_2, \sigma, j_1}, x^{i_2, \sigma, j_2}$. To see that this is an input partition, note that x^{i_1, σ, j_1} and x^{i_1, σ, j_2} differ in the same bit positions as x^{i_2, σ, j_1} and x^{i_2, σ, j_2} and that x^{i_1, σ, j_1} and x^{i_2, σ, j_1} differ in the same bit positions as x^{i_1, σ, j_2} and x^{i_2, σ, j_2} . In the first case, these bit positions are contained in the set $\{l_1, \dots, l_{m_2}\}$ and in the second case in the set $\{k_1, \dots, k_{m_1}\}$. This is a contradiction since we assumed that no input partition existed for the obfuscated function.

Suppose instead that there are no two of A, B, C that contain a switch of input. Recall from Sect. 3.1 that for a zeroizing attack to be successful, the matrix W_1 must be invertible and further, $W_0 W_1^{-1}$ must yield information about

b_σ^s encodings. If B does not contain an input switch then $x^{i,\sigma,j}$ does not depend on σ and for the zeroizing attack, we construct the matrices

$$W_0 = \mathbf{p}_{zt} \begin{pmatrix} M(x^{1,0,1}) \cdots M(x^{1,0,n}) \\ \vdots \\ M(x^{n,0,1}) \cdots M(x^{n,0,n}) \end{pmatrix} = \mathbf{p}_{zt} \begin{pmatrix} M(x^{1,1,1}) \cdots M(x^{1,1,n}) \\ \vdots \\ M(x^{n,1,1}) \cdots M(x^{n,1,n}) \end{pmatrix} = W_1.$$

Thus, $W_0 = W_1$ and we get nothing out of computing $W_0 W_1^{-1}$. If instead neither A nor C contain an input switch then $x^{i,\sigma,j}$ depends on neither i nor j . In that case we get the matrices

$$W_0 = \mathbf{p}_{zt} \begin{pmatrix} M(x^{1,0,1}) \cdots M(x^{1,0,n}) \\ \vdots \\ M(x^{n,0,1}) \cdots M(x^{n,0,n}) \end{pmatrix} = \begin{pmatrix} f_{x^{1,0,1}} \cdots f_{x^{1,0,1}} \\ \vdots \\ f_{x^{1,0,1}} \cdots f_{x^{1,0,1}} \end{pmatrix}$$

$$W_1 = \mathbf{p}_{zt} \begin{pmatrix} M(x^{1,1,1}) \cdots M(x^{1,1,n}) \\ \vdots \\ M(x^{n,1,1}) \cdots M(x^{n,1,n}) \end{pmatrix} = \begin{pmatrix} f_{x^{1,1,1}} \cdots f_{x^{1,1,1}} \\ \vdots \\ f_{x^{1,1,1}} \cdots f_{x^{1,1,1}} \end{pmatrix}.$$

These two matrices both have rank 1 and are thus not invertible. Therefore, a zeroizing attack cannot be carried out, a contradiction. \square

4 Securing Functions Against Partition Attacks

4.1 Input Partition Attacks

In this section, we define formally the notion of an input partition attack. We also define what it means for a function to be hard or impossible to partition. Since in this section we only are concerned with whether a function outputs zero or not, we consider functions with codomain $\{0, \perp\}$, where \perp represents any non-zero branching program output.

Definition 7 (Input Partition). *Let $\mathbf{v} \in \mathbb{N}^t$ be a vector and $f: \mathbb{Z}_{\mathbf{v}} \rightarrow \{0, \perp\}$ be a function. An input partition for f of degree k is a tuple*

$$\mathcal{I}_f^k = (\sigma \in S_t, \{a_i\}_{i \in [k]} \subseteq \mathbb{Z}_{\mathbf{u}_1}, \{c_j\}_{j \in [k]} \subseteq \mathbb{Z}_{\mathbf{u}_2})$$

satisfying $a_i \neq a_j$ and $c_i \neq c_j$ for all $i, j \in [k]$ with $i \neq j$ and $\sigma(\mathbf{u}_1 || \mathbf{u}_2) = \mathbf{v}$ such that for all $i, j \in [k]$,

$$f(\sigma(a_i || c_j)) = 0.$$

Definition 8 (Input Partition Attack). *For each $t \in \mathbb{N}$ let $\mathbf{v}_t \in \mathbb{N}^t$ be a vector, \mathcal{F}_t be a family of functions $f: \mathbb{Z}_{\mathbf{v}_t} \rightarrow \{0, \perp\}$, and let $\mathcal{F} = \{\mathcal{F}_t\}_{t \in \mathbb{N}}$. We say that a PPT adversary \mathcal{A} performs an input partition attack of degree k on \mathcal{F} if for a non-negligible function ϵ ,*

$$\Pr_{w, f \leftarrow \mathcal{F}_t} [\mathcal{A}(f) = \mathcal{I}_f^k \text{ is an input partition of } f \text{ of degree } k] > \epsilon(t),$$

where the probability is taken over the randomness w of \mathcal{A} and a uniform choice of f from \mathcal{F}_t .

Turning the above definition around, we can ensure security against input partition attacks if the function we obfuscate satisfies the following.

Definition 9 (Input Partition Resistance). For each $t \in \mathbb{N}$ let $\mathbf{v}_t \in \mathbb{N}^t$ be a vector, \mathcal{F}_t be a family of functions $f: \mathbb{Z}_{\mathbf{v}_t} \rightarrow \{0, \perp\}$, and let $\mathcal{F} = \{\mathcal{F}_t\}_{t \in \mathbb{N}}$. We say that \mathcal{F} is input partition resistant for degree k if no PPT adversary successfully performs an input partition attack of degree k on \mathcal{F} .

A stronger version of this is for a function to simply not admit any input partitions which would clearly make attacks requiring a partition of the input impossible.

Definition 10 (Input Unpartitionable Function). Let $\mathbf{v} \in \mathbb{N}^t$ be a vector and $f: \mathbb{Z}_{\mathbf{v}} \rightarrow \{0, \perp\}$ be a function. We say that f is input unpartitionable for degree k if it does not admit an input partition of degree k . If f is input unpartitionable for degree 2, we simply say that it is input unpartitionable.

4.2 Securing Functions

Now that we have defined the type of attack we aim to defend against, we introduce the input “stamping” function h and define what it means for h to secure a function f .

Definition 11 (Securing a Function). Let $\mathbf{v}_1 \in \mathbb{N}^{t_1}, \mathbf{v}_2 \in \mathbb{N}^{t_2}$ be vectors and write $\mathbf{v} = \mathbf{v}_1 || \mathbf{v}_2$. Let $f: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \{0, 1\}$ and $h: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \mathbb{Z}_{\mathbf{v}_2}$ be functions and construct a function $g: \mathbb{Z}_{\mathbf{v}} \rightarrow \{0, \perp\}$ as follows:

$$g(a||b) = \begin{cases} f(a), & h(a) = b \\ \perp, & h(a) \neq b. \end{cases}$$

We say that h completely secures f if g is input unpartitionable.

A slightly less strict definition is the following which defines what it means for a function family to statistically secure a function.

Definition 12 (Statistically Securing a Function). Let $\mathbf{v} \in \mathbb{N}^t$ be a vector, f be a function $f: \mathbb{Z}_{\mathbf{v}} \rightarrow \{0, 1\}$, and \mathcal{H} be a collection $\mathcal{H} = \{\mathcal{H}_\lambda\}_{\lambda \in \mathbb{N}}$ of function families such that \mathcal{H}_λ is a family of functions $h: \mathbb{Z}_{\mathbf{v}} \rightarrow \mathbb{Z}_{\mathbf{u}_\lambda}$ for some $\mathbf{u}_\lambda \in \mathbb{N}^{k_\lambda}$. We say that \mathcal{H} statistically secures f if for some negligible function ϵ and for all $\lambda \in \mathbb{N}$,

$$\Pr_{h \stackrel{\$}{\leftarrow} \mathcal{H}_\lambda} [h \text{ completely secures } f] > 1 - \epsilon(\lambda),$$

where h is sampled uniformly from \mathcal{H}_λ .

4.3 Necessary and Sufficient Conditions

In this section, we present and prove the necessary and sufficient condition on a function h in order for it to secure every function f . First, we give some useful definitions.

Definition 13. Let $k \in \mathbb{N}$ be a positive integer and define the equivalence relation \sim on $\mathbb{Z}_k \times \mathbb{Z}_k$ as follows. Two elements $(a, b), (c, d) \in \mathbb{Z}_k \times \mathbb{Z}_k$ are equivalent under \sim if and only if either $(a, b) = (c, d)$ or $a = b$ and $c = d$. We denote by \mathcal{Z}_k the set

$$\mathcal{Z}_k = \mathbb{Z}_k \times \mathbb{Z}_k / \sim.$$

For a vector $\mathbf{v} \in \mathbb{N}^t$ we write $\mathcal{Z}_{\mathbf{v}}$ for the set $\mathcal{Z}_{v_1} \times \mathcal{Z}_{v_2} \times \dots \times \mathcal{Z}_{v_t}$.

Definition 14. Let $\mathbf{v} \in \mathbb{N}^t$ be a vector. Define an operation $*$: $\mathbb{Z}_{\mathbf{v}} \times \mathbb{Z}_{\mathbf{v}} \rightarrow \mathcal{Z}_{\mathbf{v}}$ as follows. For two elements $(a_1, \dots, a_t), (b_1, \dots, b_t) \in \mathbb{Z}_{\mathbf{v}}$,

$$(a_1, \dots, a_t) * (b_1, \dots, b_t) = ((a_1, b_1), \dots, (a_t, b_t)) \in \mathcal{Z}_{\mathbf{v}}.$$

The operation $*$ is essentially a projection of two vectors $\mathbf{a}, \mathbf{b} \in \mathbb{Z}_{\mathbf{v}}$ into $\mathcal{Z}_{\mathbf{v}}$.

We now give the characterization.

Definition 15 (Safe Function). Let $\mathbf{v}_1 \in \mathbb{N}^{t_1}, \mathbf{v}_2 \in \mathbb{N}^{t_2}$ be vectors. A function $h: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \mathbb{Z}_{\mathbf{v}_2}$ is safe if for every $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \mathbb{Z}_{\mathbf{v}_1}$ it is the case that if both of the following hold:

$$\begin{aligned} x_{1,1} * x_{1,2} &= x_{2,1} * x_{2,2} \\ h(x_{1,1}) * h(x_{1,2}) &= h(x_{2,1}) * h(x_{2,2}), \end{aligned}$$

then $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$.

Theorem 1. Let $\mathbf{v}_1 \in \mathbb{N}^{t_1}, \mathbf{v}_2 \in \mathbb{N}^{t_2}$ be vectors. The function $h: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \mathbb{Z}_{\mathbf{v}_2}$ completely secures every function $f: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \{0, 1\}$ if and only if it is safe.

In order to prove Theorem 1 we first state and prove two lemmas.

Lemma 1. Let $\mathbf{v}_1 \in \mathbb{N}^{t_1}$ and $\mathbf{v}_2 \in \mathbb{N}^{t_2}$ be vectors and $\sigma \in S_{t_1+t_2}$. Let $a_1, a_2 \in \mathbb{Z}_{\mathbf{v}_1}, c_1, c_2 \in \mathbb{Z}_{\mathbf{v}_2}$, and

$$\{r_1, \dots, r_k\} = T \subseteq [t_1 + t_2], r_1 < r_2 < \dots < r_k.$$

Finally, define a function p_T such that for $x \in \mathbb{Z}_{\mathbf{v}_1 \parallel \mathbf{v}_2}$, $p_T(x) = x_{r_1} x_{r_2} \dots x_{r_k}$. Then

$$p_T(\sigma(a_1 \parallel c_1)) * p_T(\sigma(a_2 \parallel c_1)) = p_T(\sigma(a_1 \parallel c_2)) * p_T(\sigma(a_2 \parallel c_2))$$

Proof. First, note that this lemma holds in general if and only if it holds for $T = [t_1 + t_2]$. So we will simply show that

$$\sigma(a_1||c_1) * \sigma(a_2||c_1) = \sigma(a_1||c_2) * \sigma(a_2||c_2)$$

which is equivalent to showing that

$$(a_1||c_1) * (a_2||c_1) = (a_1||c_2) * (a_2||c_2).$$

However, this is trivial from the definition of the operation $*$ and the conclusion follows. □

Lemma 2. Let $\mathbf{v} \in \mathbb{N}^t$ be a vector and let $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \mathbb{Z}_{\mathbf{v}}$ be given satisfying $x_{1,1} \neq x_{1,2}$ and $x_{1,1} \neq x_{2,1}$ and

$$x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}.$$

Then there exist

$$\sigma \in S_t, a_1, a_2 \in \mathbb{Z}_{\mathbf{v}_1}, c_1, c_2 \in \mathbb{Z}_{\mathbf{v}_2}$$

with $a_1 \neq a_2, c_1 \neq c_2$, and $\sigma(\mathbf{v}_1||\mathbf{v}_2) = \mathbf{v}$ such that for every $i, j \in \{1, 2\}$,

$$\sigma(a_i||c_j) = x_{i,j}$$

Proof. Let S be the set of indices $j \in [t]$ such that $x_{1,1}^j = x_{1,2}^j$ and let D be the set of indices $j \in [t]$ such that $x_{1,1}^j \neq x_{1,2}^j$. It is clear that S and D partition the set of indices $[t]$. Since $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$, we must have the following relations:

$$\forall j \in S: x_{1,1}^j = x_{1,2}^j \text{ and } x_{2,1}^j = x_{2,2}^j \tag{1}$$

$$\forall j \in D: x_{1,1}^j = x_{2,1}^j \neq x_{1,2}^j = x_{2,2}^j \tag{2}$$

Note that because $x_{1,1} \neq x_{1,2}$, D must be non-empty. S must also be empty, otherwise $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$ would imply that $x_{1,1} = x_{2,1}$. So there must also exist an index $r \in S$ such that $x_{1,1}^r = x_{1,2}^r \neq x_{2,1}^r = x_{2,2}^r$.

Now, enumerating S and D as $S = \{m_1, \dots, m_k\}$ and $D = \{n_1, \dots, n_l\}$ with $k + l = t$, we set

$$\begin{aligned} a_i &= x_{i,1}^{m_1} x_{i,1}^{m_2} \dots x_{i,1}^{m_k} = x_{i,2}^{m_1} x_{i,2}^{m_2} \dots x_{i,2}^{m_k} \\ c_j &= x_{1,j}^{n_1} x_{1,j}^{n_2} \dots x_{1,j}^{n_k} = x_{2,j}^{n_1} x_{2,j}^{n_2} \dots x_{2,j}^{n_k}, \end{aligned}$$

for $i, j \in \{1, 2\}$ where the equalities to the right follow from (1) and (2), $a_1 \neq a_2$ because of the existence of r as above, and $c_1 \neq c_2$ from the definition of D .

Letting $\sigma \in S_t$ be the permutation such that

$$\sigma(m_1 m_2 \dots m_k n_1 n_2 \dots n_l) = 1 2 \dots t,$$

we find that $\sigma(a_i||c_j) = x_{i,j}$ for every $i, j \in \{1, 2\}$ and we are done. □

Proof (Proof of Theorem 1). Suppose h completely secures every function f and assume for contradiction that there exist $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \mathbb{Z}_{\mathbf{v}_1}$ with $x_{1,1} \neq x_{1,2}$ and $x_{1,1} \neq x_{2,1}$ such that

$$\begin{aligned} x_{1,1} * x_{1,2} &= x_{2,1} * x_{2,2} \\ h(x_{1,1}) * h(x_{1,2}) &= h(x_{2,1}) * h(x_{2,2}). \end{aligned}$$

Let f be the function satisfying $f(x) = 0$ for every $x \in \mathbb{Z}_{\mathbf{v}_1}$ and consider the function

$$g(a||b) = \begin{cases} f(a), & h(a) = b \\ \perp, & h(a) \neq b. \end{cases}$$

We clearly have

$$(x_{1,1} || h(x_{1,1})) * (x_{1,2} || h(x_{1,2})) = (x_{2,1} || h(x_{2,1})) * (x_{2,2} || h(x_{2,2}))$$

and thus, by Lemma 2 there exist

$$\sigma \in S_{t_1+t_2}, a_1, a_2 \in \mathbb{Z}_{\mathbf{u}_1} \quad c_1, c_2 \in \mathbb{Z}_{\mathbf{u}_2}$$

with $a_1 \neq a_2, c_1 \neq c_2$, and $\sigma(\mathbf{u}_1 || \mathbf{u}_2) = \mathbf{v}_1 || \mathbf{v}_2$ such that for every $i, j \in \{1, 2\}$,

$$\sigma(a_i || c_j) = x_{i,j} || h(x_{i,j}).$$

However, then $g(\sigma(a_i || c_j)) = 0$ for every $i, j \in \{1, 2\}$ which is a contradiction since g would be input unpartitionable if h completely secured the function f .

Conversely, suppose h is safe and let $f: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \{0, 1\}$ be arbitrary. Define

$$g(a||b) = \begin{cases} f(a), & h(a) = b \\ \perp, & h(a) \neq b. \end{cases}$$

and assume for contradiction that there exists an input partition for g of degree two,

$$\mathcal{I}_g^2 = (\sigma \in S_{t_1+t_2}, \{a_i\}_{i \in [k]} \subseteq \mathbb{Z}_{\mathbf{u}_1}, \{c_j\}_{j \in [k]} \subseteq \mathbb{Z}_{\mathbf{u}_2}).$$

For each $i, j \in \{1, 2\}$, write $\sigma(a_i || c_j) = x_{i,j} y_{i,j}$ with $x_{i,j} \in \mathbb{Z}_{\mathbf{v}_1}$ and $y_{i,j} \in \mathbb{Z}_{\mathbf{v}_2}$ and observe that then $h(x_{i,j}) = y_{i,j}$. Furthermore, we have

$$\begin{aligned} x_{1,1} * x_{1,2} &= x_{2,1} * x_{2,2} \\ y_{1,1} * y_{1,2} &= y_{2,1} * y_{2,2}. \end{aligned}$$

by Lemma 1. Since $h(x_{i,j}) = y_{i,j}$ it follows directly from the condition on h that either $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$. The two cases are symmetric, so assume without loss of generality that $x_{1,1} = x_{1,2}$. Then $y_{1,1} = y_{1,2}$ and we get $\sigma(a_1 || c_1) = \sigma(a_1 || c_2)$. A contradiction as we required $c_1 \neq c_2$. \square

4.4 Lower Bound on the Output Size of Safe Functions

An implementation of a safe function h to secure a function f by constructing the function g of Definition 11 results in an increase in the input size of f and further this extra input must be checked against the output of h . In the context of matrix branching programs, the check of the extra input requires a pass over the input, adding more matrices, which when initialized over multilinear maps is rather costly. Thus, knowledge about the minimal output size of a safe function is helpful in determining the costs of securing a function. In this section, we show that this output size is at least linear in the input size of f .

Theorem 3. *Let $\mathbf{v}_1 = (v_1^1, v_1^2, \dots, v_1^{t_1}) \in \mathbb{N}^{t_1}$ and $\mathbf{v}_2 = (v_2^1, v_2^2, \dots, v_2^{t_2}) \in \mathbb{N}^{t_2}$ be vectors and let $h: \mathbb{Z}_{\mathbf{v}_1} \rightarrow \mathbb{Z}_{\mathbf{v}_2}$ be a safe function. If k is such that $v_1^k = \min_{1 \leq i \leq t_1} (v_1^i)$ then*

$$\prod_{\substack{1 \leq i \leq t_1 \\ i \neq k}}^{t_1} v_1^i \leq \prod_{i=1}^{t_2} (v_2^i (v_2^i - 1) + 1).$$

Proof. Assume without loss of generality that $k = 1$. Recall that the elements of $\mathbb{Z}_{\mathbf{v}_1^i}$ are $(a, b), a \neq b$ for $a, b \in \mathbb{Z}_{v_1^i}$ together with the single element consisting of the \sim -equivalence class $\{(b, b) \mid b \in \mathbb{Z}_{v_1^i}\}$ that we denote by (a, a) . Now, consider the vector

$$y = ((b, c), (a, a), (a, a), \dots, (a, a)) \in \mathbb{Z}_{\mathbf{v}_1}$$

with $b \neq c$ and let $T = \{(x_1, x_2) \in (\mathbb{Z}_{\mathbf{v}_1})^2 \mid x_1 * x_2 = y\}$. We have $|T| = \prod_{i=2}^{t_1} v_1^i$ since for the first index there is only the choice $x_1^1 = b$ and $x_2^1 = c$ and for index $i > 1$ there are v_1^i choices for $x_1^i = x_2^i$. Now, define the function $t: T \rightarrow \mathbb{Z}_{\mathbf{v}_2}$ by $t(x_1, x_2) = h(x_1) * h(x_2)$. By the definition of a safe function, t must be injective. It follows that

$$\prod_{i=2}^{t_1} v_1^i = |T| \leq |\mathbb{Z}_{\mathbf{v}_2}| = \prod_{i=1}^{t_2} (v_2^i (v_2^i - 1) + 1).$$

□

Corollary 1. *Let $h: \{0, 1\}^{t_1} \rightarrow [k]^{t_2}$ be a safe function. Then*

$$\frac{t_1 - 1}{\log_2(k(k - 1) + 1)} \leq t_2.$$

Proof. By Theorem 3, we have

$$2^{t_1 - 1} \leq (k(k - 1) + 1)^{t_2}.$$

Taking the logarithm on both sides of the equation yields the conclusion. □

We will see in the instantiations of safe functions that while we do not achieve an optimal construction, all our constructions have output size linear in the input size of the original function and with fairly small coefficients, so they are asymptotically optimal.

5 Instantiations

We now present three instantiations for h . We first give two number theoretic functions which secure any function f , and then we give a combinatorial function that statistically secures any function f .

5.1 Number Theoretical Functions

By the necessary and sufficient condition of Theorem 1 and the definition of a safe function, it seems that a function will secure every other function if it is somewhat non-linear everywhere. This is captured in the following corollary, letting us work with functions over the integers.

Corollary 2. *Suppose the function $h: \{0, 1\}^n \rightarrow \{0, 1\}^m$ satisfies that whenever $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \{0, 1\}^n$ satisfy the relations*

$$\begin{aligned} x_{1,1} - x_{1,2} &= x_{2,1} - x_{2,2} \\ h(x_{1,1}) - h(x_{1,2}) &= h(x_{2,1}) - h(x_{2,2}). \end{aligned}$$

where we consider each term as an integer $x_{i,j} \in [2^n - 1]$ or $h(x_{i,j}) \in [2^m - 1]$ then either $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$. Then h completely secures every function $f: \{0, 1\}^n \rightarrow \{0, \perp\}$.

Proof. This follows immediately from Theorem 1 since $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$ implies $x_{1,1} - x_{1,2} = x_{2,1} - x_{2,2}$ as integers. \square

Intuitively, many functions we know and love would satisfy this as long as they have sufficient non-linearity. Here we list two examples. In terms of output size, note that these functions both produce $n + 1$ bits of output, where the minimum possible by Corollary 1 is $\frac{n-1}{\log_2 3}$. So the output size of these two functions is close to optimal.

Proposition 1. *Let p be a prime satisfying $2^n < p < 2^{n+1}$. The function $h: \{0, 1\}^n \rightarrow \{0, 1\}^{n+1}$ given by $h(x) = [x^2]_p$ completely secures every function $f: \{0, 1\}^n \rightarrow \{0, \perp\}$.*

Proof. Let $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \{0, 1\}^n$ be given and suppose $x_{1,1} - x_{2,1} = x_{1,2} - x_{2,2}$ and $h(x_{1,1}) - h(x_{2,1}) = h(x_{1,2}) - h(x_{2,2})$. We will show that $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$, concluding the proof by Corollary 2.

Directly from the conditions on the $x_{i,j}$, we get

$$\begin{aligned} (x_{1,1} + x_{2,1})(x_{1,1} - x_{2,1}) &\equiv h(x_{1,1}) - h(x_{2,1}) \\ &= h(x_{1,2}) - h(x_{2,2}) \\ &\equiv (x_{1,2} + x_{2,2})(x_{1,2} - x_{2,2}) \pmod{p}. \end{aligned}$$

This yields two cases. If $x_{1,1} - x_{2,1} = x_{1,2} - x_{2,2} = 0$ then $x_{1,1} = x_{2,1}$ and we are done. Otherwise $x_{1,1} - x_{2,1} = x_{1,2} - x_{2,2}$ is invertible modulo p since $p > 2^n$ and we get

$$x_{1,1} + x_{2,1} \equiv x_{1,2} + x_{2,2} \pmod{p}.$$

Adding $x_{1,1} - x_{2,1} = x_{1,2} - x_{2,2}$ to both sides yields $2x_{1,1} \equiv 2x_{1,2} \pmod{p}$ which is equivalent to $x_{1,1} \equiv x_{1,2} \pmod{p}$. Hence, $x_{1,1} = x_{1,2}$ since $p > 2^n$ and we are done. \square

Proposition 2. *Let p be a prime satisfying $2^n < p < 2^{n+1}$ with primitive root r . The function $h: \{0, 1\}^n \rightarrow \{0, 1\}^{n+1}$ given by $h(x) = [r^x]_p$ completely secures every function $f: \{0, 1\}^n \rightarrow \{0, 1\}$.*

Proof. Let $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \{0, 1\}^n$ be given and suppose $x_{1,1} - x_{2,1} = x_{1,2} - x_{2,2}$ and $h(x_{1,1}) - h(x_{2,1}) = h(x_{1,2}) - h(x_{2,2})$. We will show that $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$, concluding the proof by Corollary 2.

Directly from the conditions on the $x_{i,j}$, we get

$$\begin{aligned} r^{x_{1,1}}(1 - r^{x_{2,1} - x_{1,1}}) &\equiv h(x_{1,1}) - h(x_{2,1}) \\ &= h(x_{1,2}) - h(x_{2,2}) \\ &\equiv r^{x_{1,2}}(1 - r^{x_{2,2} - x_{1,2}}) \pmod{p}. \end{aligned}$$

Now we have two cases. First, if $1 - r^{x_{2,1} - x_{1,1}} = 1 - r^{x_{2,2} - x_{1,2}}$ is invertible modulo p then $r^{x_{1,1}} \equiv r^{x_{1,2}} \pmod{p}$, yielding $x_{1,1} = x_{1,2}$ since r has order $p - 1 \geq 2^n$ modulo p . Second, if $1 - r^{x_{2,1} - x_{1,1}} = 1 - r^{x_{2,2} - x_{1,2}}$ is not invertible modulo p then clearly $1 - r^{x_{2,1} - x_{1,1}} \equiv 0 \pmod{p}$ and thus, $x_{2,1} - x_{1,1} = 0$ since the order of r is $\geq 2^n$. It follows that either $x_{1,1} = x_{1,2}$ or $x_{1,1} = x_{2,1}$. \square

5.2 Permutation Hash Functions

We now discuss an instantiation which statistically secures functions f , as opposed to the functions in the previous section which completely secure f . Number theoretical functions like the ones in the previous section are difficult and expensive to implement in the setting of matrix branching programs since these do not generally support operations over a fixed field \mathbb{Z}_p . However, matrix branching programs naturally implement operations on the group of permutations, S_k . The functions we define in this section are defined in terms of randomly chosen permutations, and turns out to be a much more practical alternative. This section explains the instantiation and proves statistical security, and Sect. 5.3 describes how to implement it efficiently over CLT13.

Definition 16. *A k -Permutation Hash Function of input size n is a function $h: \{0, 1\}^n \rightarrow [k]$ randomly drawn as follows. Select permutations $\{\pi_{i,b}\}_{i \in [n], b \in \{0,1\}} \xleftarrow{\$} S_k^{2n}$ uniformly at random. For an input $x \in \{0, 1\}^n$ let*

$$\sigma_x = \prod_{i=1}^n \pi_{i,x_i}.$$

Then $h(x) = \sigma_x(1)$.

Lemma 3. *Let $x_{1,1}, x_{1,2}, x_{2,1}, x_{2,2} \in \{0, 1\}^n$ be given such that*

$$x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2},$$

$x_{1,1} \neq x_{1,2}$, and $x_{1,1} \neq x_{2,1}$. Then

$$\Pr_{h \in \mathcal{S}_{S_k}^{S_k^n}} [h(x_{1,1}) * h(x_{1,2}) = h(x_{2,1}) * h(x_{2,2})] \leq \frac{k}{(k-1)^2}.$$

Proof. Write $u = x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$ and denote by $x_{a,b}^i$ the i th bit of $x_{a,b}$ and by u_i the i th entry of u . Let $S_d \subset [n], d \in \mathcal{Z}_2$ be the set of indices $i \in [n]$ such that $u_i = d$, where we recall that the elements of \mathcal{Z}_2 are the equivalence classes $(0, 1), (1, 0), (0, 0) \sim (1, 1)$. We will denote the equivalence class containing $(0, 0)$ and $(1, 1)$ by (a, a) . Thus, the set of indices $[n]$ is partitioned into $[n] = S_{(0,1)} \cup S_{(1,0)} \cup S_{(a,a)}$.

Now, it must be the case that there is a $j \in S_{(a,a)}$ such that $x_{1,1}^j = x_{1,2}^j \neq x_{2,1}^j = x_{2,2}^j$. To see this, note that $x_{1,1}$ and $x_{2,1}$ are identical at the indices of $S_{(0,1)}$ and $S_{(1,0)}$ and if $x_{1,1}$ and $x_{2,1}$ also are identical at the indices of $S_{(a,a)}$ then $x_{1,1} = x_{2,1}$ contrary to assumption. Choose j to be maximal. We can assume without loss of generality that there is an $l \in S_{(0,1)} \cup S_{(1,0)}$ such that $l > j$ as follows. Suppose that this is not the case. Then $x_{1,1}^i = x_{1,2}^i = x_{2,1}^i = x_{2,2}^i$ for every $i > j$ since j was maximal and we must have $i \in S_{(a,a)}$ whenever $i > j$. Now, consider the equation $u' = x_{1,1} * x_{2,1} = x_{1,2} * x_{2,2}$ where we simply swap $x_{1,2}$ and $x_{2,1}$ from our original expression. Let $S'_d \subset [n], d \in \mathcal{Z}_2$ be the set of indices $i \in [n]$ such that $u'_i = d$. In this dual situation, $j \in [n] \setminus S'_{(a,a)}$ and still $x_{1,1}^i = x_{1,2}^i = x_{2,1}^i = x_{2,2}^i$ for every $i > j$. Further, we can find a new maximal $j' \in S'_{(a,a)}$ with $j' < j$ such that $x_{1,1}^{j'} \neq x_{1,2}^{j'}$. Thus, in the dual situation j is the l that we were seeking in the original case. From this it follows that we without loss of generality can choose $l > j$ as above.

Define the following permutations, noting that $x_{1,1}^i = x_{2,1}^i$ and $x_{1,2}^i = x_{2,2}^i$ for $i > j$.

$$\begin{aligned} \tau &= \prod_{i=j+1}^n \pi_{i, x_{1,1}^i} \\ \sigma &= \prod_{i=j+1}^n \pi_{i, x_{1,2}^i} \\ \gamma_{b,c} &= \prod_{i=1}^{j-1} \pi_{i, x_{b,c}^i}, b, c \in \{1, 2\}. \end{aligned}$$

Then letting $b = x_{1,1}^j = x_{1,2}^j$ we get

$$\begin{aligned} h(x_{1,1}) &= \gamma_{1,1} \circ \pi_{j,b} \circ \tau(1), & h(x_{2,1}) &= \gamma_{2,1} \circ \pi_{j,1-b} \circ \tau(1) \\ h(x_{1,2}) &= \gamma_{1,2} \circ \pi_{j,b} \circ \sigma(1), & h(x_{2,2}) &= \gamma_{2,2} \circ \pi_{j,1-b} \circ \sigma(1). \end{aligned}$$

Now, since $x_{1,1}^l = x_{2,1}^l \neq x_{1,2}^l = x_{2,2}^l$, the permutations $\pi_{l,x_{1,1}^l} = \pi_{l,x_{2,1}^l}$ and $\pi_{l,x_{1,2}^l} = \pi_{l,x_{2,2}^l}$ are chosen independently of each other. Thus, it follows from $l > j$ that over the choice of $\{\pi_{i,b}\}$ the permutation τ and the permutation σ are independently and uniformly distributed. Writing $(a, b) = (\tau(1), \sigma(1)) \in \mathbb{Z}_k \times \mathbb{Z}_k$, this means that (a, b) will be uniformly distributed in $\mathbb{Z}_k \times \mathbb{Z}_k$ over the choice of $\{\pi_{i,b}\}$. Thus, with probability $\frac{k-1}{k}$ we have $a \neq b$. We will assume that from now on. The above equations now become

$$\begin{aligned} h(x_{1,1}) &= \gamma_{1,1} \circ \pi_{i,b}(a), & h(x_{2,1}) &= \gamma_{2,1} \circ \pi_{i,1-b}(a) \\ h(x_{1,2}) &= \gamma_{1,2} \circ \pi_{i,b}(b), & h(x_{2,2}) &= \gamma_{2,2} \circ \pi_{i,1-b}(b). \end{aligned}$$

Noting that $\pi_{i,b}$ and $\pi_{i,1-b}$ are chosen independently we get that for some $(q, r), (s, t) \in T$ for $T = \mathbb{Z}_k \times \mathbb{Z}_k \setminus \{(a, a) \mid a \in \mathbb{Z}_k\}$ which are independent of each other and each uniformly distributed over T over the choice of $\{\pi_{i,b}\}$, we have

$$\begin{aligned} h(x_{1,1}) &= \gamma_{1,1}(q), & h(x_{2,1}) &= \gamma_{2,1}(s) \\ h(x_{1,2}) &= \gamma_{1,2}(r), & h(x_{2,2}) &= \gamma_{2,2}(t). \end{aligned}$$

The γ s are chosen independently of the other permutations, so even conditional on the particular choice of the γ s the distribution of (q, r) and (s, t) are independent and uniformly distributed across T . This means that the pairs $(h(x_{1,1}), h(x_{1,2}))$ and $(h(x_{2,1}), h(x_{2,2}))$ are independent of each other and are uniformly distributed over subsets $U_1, U_2 \subset [k] \times [k]$, respectively, of size $|T| = k(k - 1)$.

Now, taking into account the case we disregarded in the beginning where $a = b$ which has probability $\frac{1}{k}$, we can write

$$\begin{aligned} \Pr_{h \stackrel{\$}{\leftarrow} S_k^{2n}} [h(x_{1,1}) * h(x_{1,2}) = h(x_{2,1}) * h(x_{2,2})] &\leq \frac{1}{k} + \Pr_{\substack{(\alpha, \beta) \stackrel{\$}{\leftarrow} U_1 \\ (\gamma, \delta) \stackrel{\$}{\leftarrow} U_2}} [(\alpha, \beta) \sim (\gamma, \delta)] \\ &= \frac{1}{k} + \frac{1}{k(k - 1)} \sum_{(\alpha, \beta) \in U_1} \Pr_{(\gamma, \delta) \stackrel{\$}{\leftarrow} U_2} [(\alpha, \beta) \sim (\gamma, \delta)] \\ &= \frac{1}{k} + \frac{R}{k(k - 1)} \end{aligned}$$

Splitting in the case when $\alpha = \beta$ and $\alpha \neq \beta$, where we know that the former happens in at most k cases, we can calculate

$$\begin{aligned} R &= \sum_{(\alpha, \beta) \in U_1} \Pr_{(\gamma, \delta) \stackrel{\$}{\leftarrow} U_2} [(\alpha, \beta) \sim (\gamma, \delta)] \\ &\leq \sum_{\substack{(\alpha, \beta) \in U_1 \\ \alpha = \beta}} \Pr_{(\gamma, \delta) \stackrel{\$}{\leftarrow} U_2} [\gamma = \delta] + \sum_{\substack{(\alpha, \beta) \in U_1 \\ \alpha \neq \beta}} \Pr_{(\gamma, \delta) \stackrel{\$}{\leftarrow} U_2} [\alpha = \gamma \text{ and } \beta = \delta] \\ &\leq k \cdot \frac{1}{k - 1} + k(k - 1) \cdot \frac{1}{k(k - 1)} = \frac{2k - 1}{k - 1}. \end{aligned}$$

It follows that

$$\Pr_{\{\pi_{i,b}\}} [h(x_{2,1} * h(x_{2,2})) = h(x_{1,1}) * h(x_{1,2})] \leq \frac{1}{k} + \frac{2k - 1}{k(k - 1)^2} = \frac{k}{(k - 1)^2}.$$

□

Lemma 4. Draw k -permutation hash functions on n input bits, h_1, \dots, h_t independently at random. Then the probability that there exists $\{x_{b,c}\}_{b,c \in \{1,2\}} \subseteq \{0, 1\}^n$ with $x_{1,1} \neq x_{1,2}$, $x_{1,1} \neq x_{2,1}$, $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$, and $h_i(x_{1,1}) * h_i(x_{1,2}) = h_i(x_{2,1}) * h_i(x_{2,2})$ for every $i \in [t]$ is strictly less than $\frac{k^t \cdot 6^n}{(k-1)^{2t}}$.

Proof. Let $u \in (\mathcal{Z}_2)^n$ be given and let s be the number of entries of u that are (a, a) , denoting the equivalence classes of \mathcal{Z}_2 as before by $(0, 1)$, $(1, 0)$, and (a, a) . The number of choices for $\{x_{b,c}\}_{b,c \in \{1,2\}} \subseteq \{0, 1\}^n$ such that $u = x_{1,1} * x_{1,2} = *x_{2,1} * x_{2,2}$ is 2^{2s} since for all entries $i \in [n]$ such that $u_i = (0, 1)$ or $u_i = (1, 0)$, $x_{1,1}^i = x_{2,1}^i$ and $x_{1,2}^i = x_{2,2}^i$ are fixed and for all entries $i \in [n]$ such that $u_i = (a, a)$ we have $x_{1,1}^i = x_{2,1}^i$ and $x_{1,2}^i = x_{2,2}^i$, but they are not fixed. Now, note that the number of $u \in (\mathcal{Z}_2)^n$ with exactly s (a, a) -entries is $2^{n-s} \binom{n}{s}$. Summing over all possible u we get the total number of choices $\{x_{b,c}\}_{b,c \in \{1,2\}} \subseteq \{0, 1\}^n$ with $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$ to be

$$\sum_{s=0}^n 2^{n-s} \binom{n}{s} 2^{2s} = 2^n \sum_{s=0}^n 2^s \binom{n}{s} = 6^n.$$

By Lemma 3 the probability that any single choice of $\{x_{b,c}\}_{b,c \in \{1,2\}} \subseteq \{0, 1\}^n$ such that $x_{1,1} \neq x_{1,2}$, $x_{1,1} \neq x_{2,1}$ and $x_{1,1} * x_{1,2} = x_{2,1} * x_{2,2}$ satisfies $h_i(x_{1,1}) * h_i(x_{1,2}) = h_i(x_{2,1}) * h_i(x_{2,2})$ for all $i \in [t]$ is strictly less than $\left(\frac{k}{(k-1)^2}\right)^t$. Thus, our conclusion follows immediately by the union bound. □

For the next theorem we define a function that combines several permutation hash functions into one. Choose k -permutation hash functions h_1, \dots, h_t as discussed above, and define the main hash function $h(x) = h_1(x) || h_2(x) || \dots || h_t(x)$.

Theorem 4. Let $k \geq 3$. If $t \geq \frac{(1 + \log_2(3))n + \lambda}{2 \log_2(k-1) - \log_2(k)}$ then the function h as defined above statistically secures every function $f: \{0, 1\}^n \rightarrow \{0, 1\}$.

Proof. Fix $k \geq 3$ and n . By Lemma 4, h statistically secures every function if

$$\frac{k^t \cdot 6^n}{(k - 1)^{2t}} \leq 2^{-\lambda}.$$

Taking logarithms on both sides, this is equivalent to

$$\log_2(k)t + (1 + \log_2(3))n - 2t \log_2(k - 1) \leq -\lambda.$$

Rearranging, this yields that h statistically secures every function for

$$t \geq \frac{(1 + \log_2(3))n + \lambda}{2 \log_2(k - 1) - \log_2(k)}.$$

□

5.3 Implementing Permutation Hash Functions over CLT13

We now describe how to efficiently secure a branching program over CLT using permutation hash functions. We first describe how to construct a branching program that takes an input $u||v$ and checks whether $v = h_i(u)$ for a single hash function h_i from the previous section. We then describe a technique that allows evaluating branching programs in parallel as long as they have the same input function. Finally, we use this technique to efficiently add a securing permutation hash function to any matrix branching program over CLT13.

Implementing One Permutation Hash Function Check. Assume we are given a k -permutation hash function $h = h_i$ of input size n as in the previous section, with the corresponding permutations $\{\pi_{i,b}\}_{i,b}$. We construct a branching program BP^h over some $R \cong \mathbb{Z}_p$ that works over inputs in \mathbb{Z}_v , where $v = [2, \dots, 2, k] \in \mathbb{Z}^{n+1}$. This branching program will compute h over the first n bits in the input and then check if the result matches the final piece of input.

In the following sections we denote a branching program of length l that works over inputs in \mathbb{Z}_v by the tuple $(\mathbf{mat}, M_0, M_{l+1}, \mathbf{inp})$, where $\mathbf{mat}(i)$ is an indexed family $\{M_{i,c}\}_{c \in \mathbb{Z}_{v_i}}$ for all $i \in [l]$. M_0 and M_{l+1} are “bookend” vectors. This branching program is evaluated over an input $x \in \mathbb{Z}_v$ by computing the following product:

$$M_0 \times \prod_{i=1}^n M_{i, x_{\mathbf{inp}(i)}} \times M_{l+1}.$$

For a k -permutation hash function h , let

$$\text{BP}^h = (\mathbf{mat}^h, M_0^h, M_{n+2}^h, \mathbf{inp}^h).$$

The components of BP^h are defined as follows:

1. $\mathbf{mat}^h(1) = \{M_{1,c}^h\}_{c \in \mathbb{Z}_k}$, where $M_{1,c}^h \in M_k(R)$ is the permutation matrix corresponding to the transposition $(1\ c)$.
2. $\mathbf{mat}^h(i) = \{M_{i,b}^h\}_{b \in \{0,1\}}$ for $2 \leq i \leq n + 1$, where $M_{i,b}^h \in M_k(R)$ is the permutation matrix corresponding to $\pi_{i-1,b}$.
3. $M_0^h = [1, 0, \dots, 0] \in R^k$.
4. $M_{n+2}^h = [0, 1, \dots, 1]^T \in R^k$.
5. $\mathbf{inp}^h(i) = \begin{cases} n + 1 & i = 1 \\ i - 1 & 2 \leq i \leq n + 1. \end{cases}$

Consider an evaluation of the branching program BP^h over an input $u||v$, where $u \in \{0, 1\}^n$ and $v \in \mathbb{Z}_k$. The result is of the form

$$M_0^h \times M_{1,v}^h \times \prod_{i=2}^{n+1} M_{i,u_{i-1}}^h \times M_{n+2}^h. \tag{3}$$

The product $\prod_{i=2}^{n+1} M_{i,u_{i-1}}^h \times M_{n+2}^h$ results in a column vector with a 0 at position $h(u)$ and 1s in every other position. The product of this result with $M_{1,v}^h$ produces $[0, 1, \dots, 1]^T$ if and only if $h(u) = v$ and otherwise produces a vector with a 0 in a position other than the first and 1s everywhere else. Multiplying by M_0^h thus produces 0 if and only if $h(u) = v$. In conclusion, evaluating BP^h on input $u||v$ outputs 0 if and only if $h(u) = v$.

Parallel Branching Programs. The CLT13 multilinear map uses a ring of composite order, which allows for a certain type of parallel branching program computation. Namely, we can construct a branching program where each step actuality encodes steps for several branching programs, and the parent branching program evaluates to zero if and only if all of its underlying branching programs do. In this section, we describe how to construct such a parallel computation.

Let n be the dimension chosen by the CLT13 instantiation based on the security parameter. This number is the number of prime factors of the ring order. (We assume it is squarefree.) Let $\text{BP}_1, \text{BP}_2, \dots, \text{BP}_n$ be the set of branching programs we want to evaluate in parallel. Following [GLW14], we will work over a CLT13 instantiation of dimension n^2 . Recall that the plaintext ring for a CLT13 instantiation is of the form $\mathbb{Z}_g \cong \bigoplus_{i=1}^n \mathbb{Z}_{g_i}$ for primes g_i . In our case we think of the ring as being $\mathbb{Z}_g \cong \bigoplus_{i=1}^n \mathbb{Z}_{G_i}$, where each G_i is the product of n primes $g_{i,j}$. We will perform the evaluation of each branching program in a different component \mathbb{Z}_{G_i} . This variant of CLT13 is described in Section B6 of [GLW14].

We make several assumptions restricting the types of branching programs that we can execute in parallel. First, assume they are all of the same length l and all take inputs from \mathbb{Z}_v . Second, assume the matrices of BP_i are defined over the ring \mathbb{Z}_{G_i} for all i . We also assume the matrices of all the BP_i are of the same size, which is without loss of generality since we can pad them with identity matrices. Finally, assume every BP_i has the same input function \mathbf{inp} .

Let $\text{BP}_i = (\mathbf{mat}_i, M_{i,0}, M_{i,l+1}, \mathbf{inp})$, where $\mathbf{mat}_i(j) = \{M_{i,j,c}\}_{c \in \mathbb{Z}_{v_{\mathbf{inp}(j)}}$. We construct a new branching program $\text{BP}' = (\mathbf{mat}', M'_0, M'_{l+1}, \mathbf{inp})$ over the ring \mathbb{Z}_g , where $\mathbf{mat}'(j) = \{M'_{j,c}\}_{c \in \mathbb{Z}_{v_{\mathbf{inp}(j)}}$ with $M'_{j,c} \equiv M_{i,j,c} \pmod{G_i}$ for all $i \in [n]$, $j \in [l]$, and $c \in \mathbb{Z}_{v_j}$, and additionally $M'_0 \equiv M_{i,0} \pmod{G_i}$ and $M'_{l+1} \equiv M_{i,l+1} \pmod{G_i}$ for all $i \in [n]$. If we evaluate the branching program BP' on $x \in \mathbb{Z}_v$ as the product

$$M'_0 \times \prod_{j=1}^l M'_{j, \mathbf{inp}(j)} \times M'_{l+1} \pmod{g},$$

the result, $\text{BP}'(x)$, is zero if and only if

$$M_{i,0} \times \prod_{j=1}^l M_{i,j, \mathbf{inp}(j)} \times M_{i,l+1} \equiv 0 \pmod{G_i}$$

for all $i \in [n]$.

Securing an Arbitrary Branching Program. Assume we have a branching program $\text{BP} = (\mathbf{mat}, M_0, M_{l+1}, \mathbf{inp})$, $\mathbf{mat}(j) = \{M_{j,b}\}_{b \in \{0,1\}}$ over $\{0, 1\}^n$ which we would like to secure. We need to construct a new branching program BP' which computes BP but also requires an additional section of input which should be a hash of the first part. BP' must check whether the hash is valid and must always return a nonzero value if it is not.

More formally, let u be an input to BP. Let h_1, \dots, h_t be k -permutation hash functions on $|u|$ bits. BP' takes input $u||v$ and checks whether $v_i = h_i(u)$ for all $i \in [t]$. If so BP' returns $\text{BP}(u)$, and if not BP' returns some nonzero value.

Let h_i be implemented by the branching program

$$\text{BP}^{h_i} = (\mathbf{mat}^{h_i}, M_{b_1}^{h_i}, M_{b_2}^{h_i}, \mathbf{inp}^{h_i}),$$

where $\mathbf{mat}^{h_i}(1) = \{M_{1,c}^{h_i}\}_{c \in \mathbb{Z}_k}$ and $\mathbf{mat}^{h_i}(j) = \{M_{j,b}^{h_i}\}_{b \in \{0,1\}}$ for $2 \leq i \leq n + 1$. We need to modify this branching program so that instead of taking an input $u||v \in \mathbb{Z}_{[2, \dots, 2, k]}$ of length $n + 1$, it takes an input $u||v \in \mathbb{Z}_{[2, \dots, 2, k, \dots, k]}$ of length $n + t$ and checks whether $v_i = h_i(u)$. We can do this by altering the input function \mathbf{inp}^{h_i} to set $\mathbf{inp}^{h_i}(1) = n + i$, but this would result in the branching programs BP^{h_i} having different input functions for different values of i , which is not compatible with parallel branching program evaluation. So instead we pad the branching program so that the first t entries are all the identity matrix except for the i 'th entry which is $\{M_{1,c}^{h_i}\}_{c \in \mathbb{Z}_k}$. Then the input function can be set to be the same for all i . Specifically, we redefine \mathbf{mat}^{h_i} as follows:

- $\mathbf{mat}^{h_i}(i) = \{M_{1,c}^{h_i}\}_{c \in \mathbb{Z}_k}$
- $\mathbf{mat}^{h_i}(j) = \{I_k\}_{c \in \mathbb{Z}_k}$ for all $1 \leq j \leq t, j \neq i$
- $\mathbf{mat}^{h_i}(j) = \{M_{j-t+1,b}^{h_i}\}_{b \in \{0,1\}}$ for all $t + 1 \leq j \leq t + n$

and we redefine \mathbf{inp}^{h_i} as follows:

$$\mathbf{inp}^{h_i}(j) = \mathbf{inp}^h(j) = \begin{cases} n + j & 1 \leq j \leq t \\ j - t & t + 1 \leq j \leq t + n. \end{cases}$$

We are now ready to use parallel branching program evaluation to combine the hash function checks with the original branching program functionality. We use $t + 1$ branching programs, one of which is a modified version of BP and the others are modified versions of the BP^{h_i} . Every modified branching program will have length $t + l$ and will share the same input function \mathbf{inp}' , so as to facilitate parallel evaluation.

We first define the new input function:

$$\mathbf{inp}'(j) = \begin{cases} n + j & 1 \leq j \leq t \\ \mathbf{inp}(j - t) & t + 1 \leq j \leq t + n \end{cases}$$

The reasoning for this definition will become clear shortly. We modify $\text{BP} = (\mathbf{mat}, M_0, M_{l+1}, \mathbf{inp})$ by padding the branching program with identity matrices

at the beginning while leaving the rest of the program unchanged. So $\mathbf{mat}(j) = I$ for $1 \leq i \leq t$, and $\mathbf{mat}(j) = \{M_{j-t,b}\}_{b \in \{0,1\}}$ for $t+1 \leq j \leq t+l$. Note that BP should now be evaluated using the input function \mathbf{inp}' .

Finally, we describe how we modify \mathbf{BP}^{h_i} to work with the input function \mathbf{inp}' . The problem with the definition of \mathbf{BP}^{h_i} given above is that the input function during the latter part of the branching program, where $j > t$, does not match the input function of BP. We could fix this by padding BP with more identity matrices so that the computation of the h_i and the computation of BP would happen sequentially, but this would add to the total length of the resulting parallel branching program. Instead, we make an observation about the computation of h_i which allows for some flexibility in how we define the input function to the program. We will use these observations to rearrange \mathbf{BP}^{h_i} so that it matches \mathbf{inp}' exactly.

We observe that changing the order in which we read the input does not affect whether $h = h_1 || \dots || h_t$ secures a function or not since this is equivalent to for each h_i to permute the order of composition of the permutations of h_i . Since each of the permutations of h_i are chosen uniformly at random, this does not affect the distribution of h_i .

Given this observation, we can redefine \mathbf{mat}_{h_i} as follows without changing its functionality. Let f_j be the smallest r such that $\mathbf{inp}(r) = j$ (we assume that BP reads all of its input at some point such that this is well-defined). Then we set

- $\mathbf{mat}^{h_i}(t + f_j) = \{M_{j+1,b}^{h_i}\}_{b \in \{0,1\}}$ for all $1 \leq j \leq n$.
- $\mathbf{mat}^{h_i}(t + r) = \{I\}_{b \in \{0,1\}}$ for all $r \in [l] \setminus \{f_j\}_{j \in [n]}$.
- $\mathbf{mat}^{h_i}(i) = \{M_{1,c}^{h_i}\}_{c \in \mathbb{Z}_k}$.
- $\mathbf{mat}^{h_i}(j) = \{I_k\}_{c \in \mathbb{Z}_k}$ for all $1 \leq j \leq t, j \neq i$.

Thus we have $t+1$ branching programs which now share the same input function, and evaluating the branching programs in parallel as described above achieves the functionality of \mathbf{BP}' as desired.

Incurred Overhead. There are three ways in which our technique for securing obfuscation against CLT zeroizing attacks can increase the size of the program. We explain and address each of these below.

First, the parallelization requires more primes in the CLT instantiation. As described above, we use a variant of CLT13 from [GLW14] where there is an incurred increase in the dimension. This is to allow secure parallel branching program execution. We note, however, that the number of parallel executions needed, t , is less than the dimension n needed for security in the original CLT13 construction for every interesting branching program (e.g. every branching program that reads every bit of its input).

Second, checking the result of the permutation hash functions requires making the branching program longer by t matrices. This increases the degrees of multilinearity by t .

Third, if the original branching program had breadth q , i.e. each matrix of the branching program was a q by q matrix, then our procedure yields a branching program of breadth $\max\{q, k\}$. So having a large choice of k might increase the size of the branching program. Note that Theorem 4 implies a tradeoff between t and k .

We explore this further with a concrete example. Let us assume that the BP takes 5 passes over its input and that BP has breadth 5. Setting the parameter k , there is a trade-off between the number of encodings needed and the levels of multilinearity. The latter decides the size of each encoding. If we simply set $k = 5$ then the breadth of the branching program stays the same and we get roughly $2n$ extra levels of multilinearity. In our concrete example, this leads to a 40% increase in multilinearity and a 7 factor increase in the number of encodings. If instead we let $k = 2^{10}$, we see a mere 7% increase in the levels of multilinearity. The number of encodings increases drastically since the breadth of the branching program becomes about 2^{10} . However, the most efficient current obfuscation implementation [BISW17] Boneh et al. uses branching programs with breadth in excess of 2^{10} . Thus, for practical implementation this is not unreasonable.

Acknowledgements. We thank Jean-Sebastien Coron for pointing out a technical oversight in an earlier version of this work. We also thank the anonymous referees for their comments.

References

- [AGIS14] Ananth, P., Gupta, D., Ishai, Y., Sahai, A.: Optimizing obfuscation: avoiding barrington’s theorem. In: Proceedings of the 2014 ACM SIGSAC Conference on Computer and Communications Security, CCS 2014, pp. 646–658. ACM, New York (2014)
- [AS17] Ananth, P., Sahai, A.: Projective arithmetic functional encryption and indistinguishability obfuscation from degree-5 multilinear maps. In: Coron, J.-S., Nielsen, J.B. (eds.) EUROCRYPT 2017 Part I. LNCS, vol. 10210, pp. 152–181. Springer, Cham (2017). doi:[10.1007/978-3-319-56620-7_6](https://doi.org/10.1007/978-3-319-56620-7_6)
- [BGK+14] Barak, B., Garg, S., Kalai, Y.T., Paneth, O., Sahai, A.: Protecting obfuscation against algebraic attacks. In: Nguyen, P.Q., Oswald, E. (eds.) EUROCRYPT 2014. LNCS, vol. 8441, pp. 221–238. Springer, Heidelberg (2014). doi:[10.1007/978-3-642-55220-5_13](https://doi.org/10.1007/978-3-642-55220-5_13)
- [BISW17] Boneh, D., Ishai, Y., Sahai, A., Wu, D.J.: Lattice-based SNARGs and their application to more efficient obfuscation. In: Coron, J.-S., Nielsen, J.B. (eds.) EUROCRYPT 2017 Part III. LNCS, vol. 10212, pp. 247–277. Springer, Cham (2017). doi:[10.1007/978-3-319-56617-7_9](https://doi.org/10.1007/978-3-319-56617-7_9)
- [BMSZ16] Badrinarayanan, S., Miles, E., Sahai, A., Zhandry, M.: Post-zeroizing obfuscation: new mathematical tools, and the case of evasive circuits. In: Fischlin, M., Coron, J.-S. (eds.) EUROCRYPT 2016 Part II. LNCS, vol. 9666, pp. 764–791. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-49896-5_27](https://doi.org/10.1007/978-3-662-49896-5_27)

- [CGH+15] Coron, J.-S., et al.: Zeroizing without low-level zeroes: new MMAP attacks and their limitations. In: Gennaro, R., Robshaw, M. (eds.) CRYPTO 2015 Part I. LNCS, vol. 9215, pp. 247–266. Springer, Heidelberg (2015). doi:[10.1007/978-3-662-47989-6_12](https://doi.org/10.1007/978-3-662-47989-6_12)
- [CGH17] Chen, Y., Gentry, C., Halevi, S.: Cryptanalyses of candidate branching program obfuscators. In: Coron, J.-S., Nielsen, J.B. (eds.) EUROCRYPT 2017 Part III. LNCS, vol. 10212, pp. 278–307. Springer, Cham (2017). doi:[10.1007/978-3-319-56617-7_10](https://doi.org/10.1007/978-3-319-56617-7_10)
- [CHL+15] Cheon, J.H., Han, K., Lee, C., Ryu, H., Stehlé, D.: Cryptanalysis of the multilinear map over the integers. In: Oswald, E., Fischlin, M. (eds.) EUROCRYPT 2015 Part I. LNCS, vol. 9056, pp. 3–12. Springer, Heidelberg (2015). doi:[10.1007/978-3-662-46800-5_1](https://doi.org/10.1007/978-3-662-46800-5_1)
- [CLLT17] Coron, J.-S., Lee, M.S., Lepoint, T., Tibouchi, M.: Zeroizing attacks on indistinguishability obfuscation over CLT13. In: Fehr, S. (ed.) PKC 2017 Part I. LNCS, vol. 10174, pp. 41–58. Springer, Heidelberg (2017). doi:[10.1007/978-3-662-54365-8_3](https://doi.org/10.1007/978-3-662-54365-8_3)
- [CLT13] Coron, J.-S., Lepoint, T., Tibouchi, M.: Practical multilinear maps over the integers. In: Canetti, R., Garay, J.A. (eds.) CRYPTO 2013 Part I. LNCS, vol. 8042, pp. 476–493. Springer, Heidelberg (2013). doi:[10.1007/978-3-642-40041-4_26](https://doi.org/10.1007/978-3-642-40041-4_26)
- [GGH13a] Garg, S., Gentry, C., Halevi, S.: Candidate multilinear maps from ideal lattices. In: Johansson, T., Nguyen, P.Q. (eds.) EUROCRYPT 2013. LNCS, vol. 7881, pp. 1–17. Springer, Heidelberg (2013). doi:[10.1007/978-3-642-38348-9_1](https://doi.org/10.1007/978-3-642-38348-9_1)
- [GGH+13b] Garg, S., Gentry, C., Halevi, S., Raykova, M., Sahai, A., Waters, B.: Candidate indistinguishability obfuscation and functional encryption for all circuits. In: 2013 IEEE 54th Annual Symposium on Foundations of Computer Science (FOCS), pp. 40–49. IEEE (2013)
- [GGH15] Gentry, C., Gorbunov, S., Halevi, S.: Graph-induced multilinear maps from lattices. In: Dodis, Y., Nielsen, J.B. (eds.) TCC 2015 Part II. LNCS, vol. 9015, pp. 498–527. Springer, Heidelberg (2015). doi:[10.1007/978-3-662-46497-7_20](https://doi.org/10.1007/978-3-662-46497-7_20)
- [GLSW15] Gentry, C., Lewko, A.B., Sahai, A., Waters, B.: Indistinguishability obfuscation from the multilinear subgroup elimination assumption. In: IEEE 56th Annual Symposium on Foundations of Computer Science, FOCS 2015, Berkeley, CA, USA, 17–20 October 2015, pp. 151–170 (2015)
- [GLW14] Gentry, C., Lewko, A., Waters, B.: Witness encryption from instance independent assumptions. In: Garay, J.A., Gennaro, R. (eds.) CRYPTO 2014 Part I. LNCS, vol. 8616, pp. 426–443. Springer, Heidelberg (2014). doi:[10.1007/978-3-662-44371-2_24](https://doi.org/10.1007/978-3-662-44371-2_24)
- [GMM+16] Garg, S., Miles, E., Mukherjee, P., Sahai, A., Srinivasan, A., Zhandry, M.: Secure obfuscation in a weak multilinear map model. In: Hirt, M., Smith, A. (eds.) TCC 2016 Part II. LNCS, vol. 9986, pp. 241–268. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-53644-5_10](https://doi.org/10.1007/978-3-662-53644-5_10)
- [MSZ16] Miles, E., Sahai, A., Zhandry, M.: Annihilation attacks for multilinear maps: cryptanalysis of indistinguishability obfuscation over GGH13. In: Robshaw, M., Katz, J. (eds.) CRYPTO 2016 Part II. LNCS, vol. 9815, pp. 629–658. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-53008-5_22](https://doi.org/10.1007/978-3-662-53008-5_22)